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COMET DIS

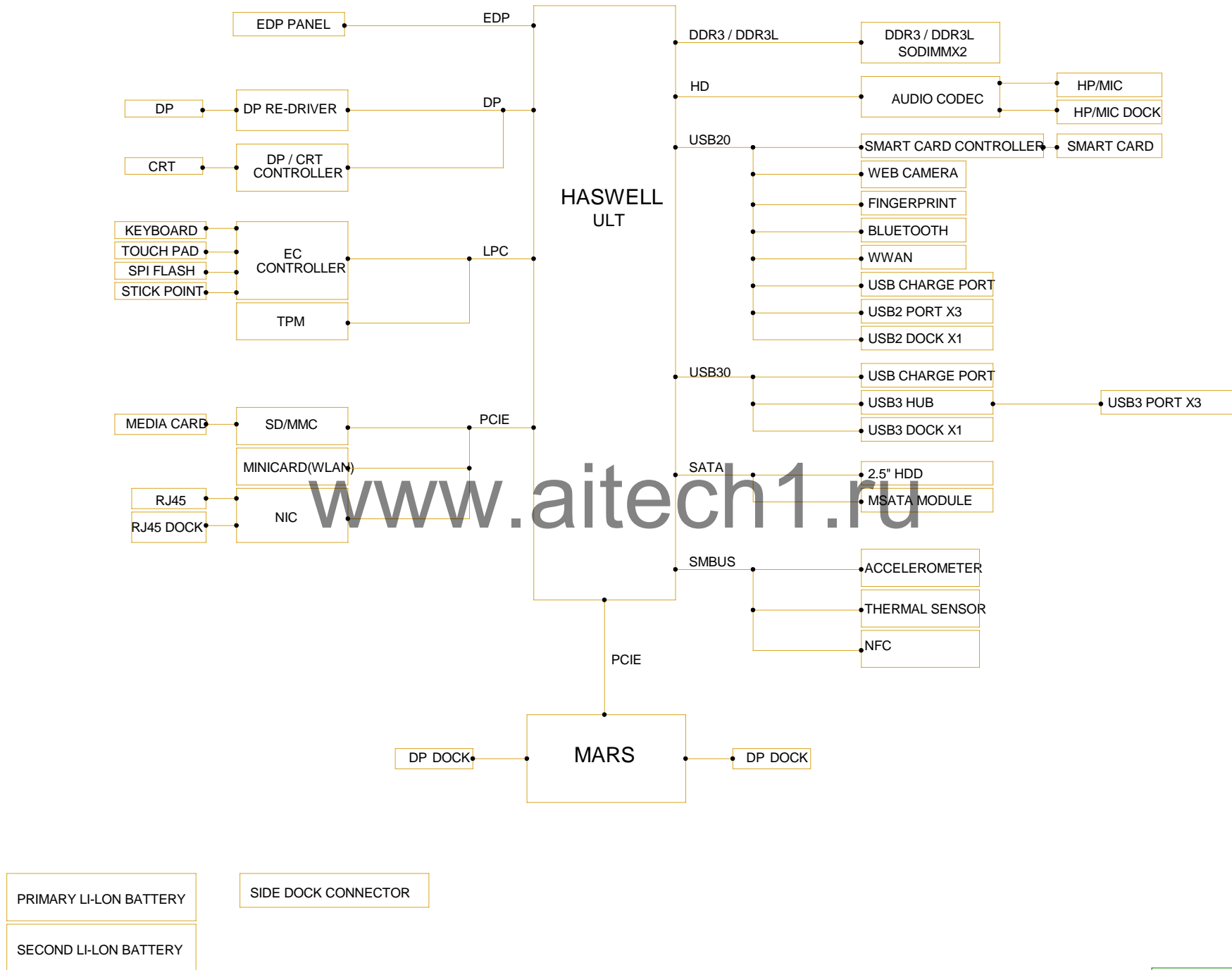
SI1 BUILD

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2012.11.30

21-OCT-2002		
DATE	CHANGE NO.	REV

DRAWER						INVENTEC			
DESIGN						TITLE			
CHECK						MODEL, PROJECT, FUNCTION			
RESPONSIBLE						COMET			
SIZE: A4						SIZE: C	CODE: CS	DOC NUMBER: 6310XXXX-0-0	REV: X01
FILE NAME: COMET DIS						SHEET			
PIN: 00000000000000000000									



INVENTEC

TITLE			
MODEL, PROJECT, FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
C	CS	131000000-0-0	201
SHEET		REV	
1		1	

CHANGE BY: XXXX DATE: 21-OCT-2002

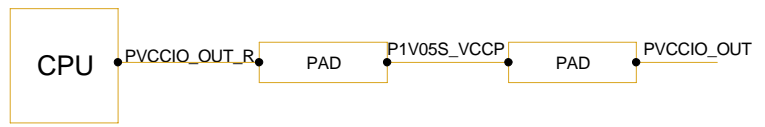
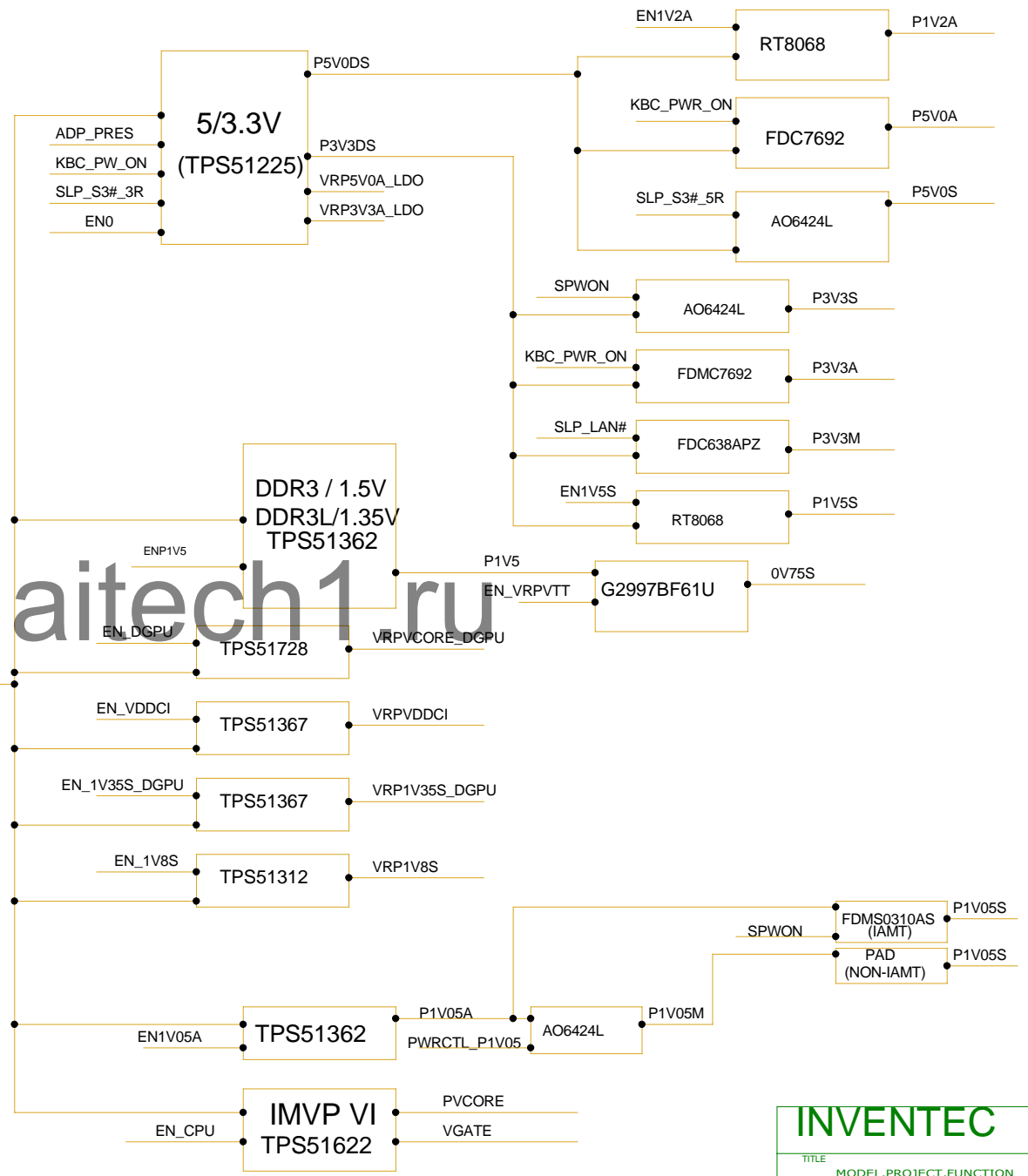
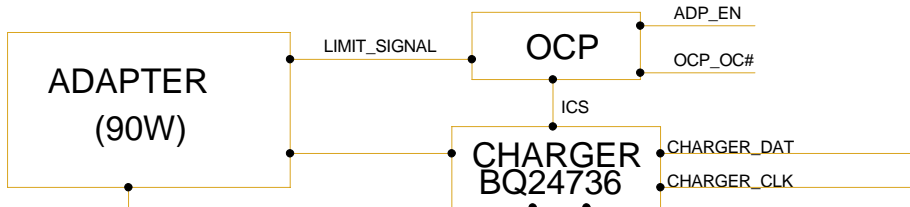
TABLE OF CONTENTS

01.PROJECT NAME	26.XDP & ME CONN.	51.KBC, SPI
02.BLOCK DIAGRAM	27.HASWELL_1 (MISC,JTAG)	52.KEYBOARD
03.TABLE OF CONTENTS	28.HASWELL_2 (LPC,SPI,SMBUS,CLINK,PM)	53.TPM
04.POWER BLOCK DIAGRAM	29.HASWELL_3 (GPIO)	54.LAN
05.SYSTEM POWER(CHARGER)	30.HASWELL_4 (DP,EDP)	55.WLAN/BT SLOT
06.SYSTEM POWER(BATT SELECTOR)	31.HASWELL_5 (DDR)	56.WWAN SLOT, SIM SLOT
07.SYSTEM POWER(OCF)	32.HASWELL_6 (PCIE,USB)	57.DOCKING
08.SYSTEM POWER(P3V3A&P5V0A)	33.HASWELL_7 (RTC,AUDIO,SATA,JTAG)	58.B TO B CONN, POINT STICK CONN
09.P3V3A&P5V0A_CHG PORT	34.HASWELL_8 (CLK)	59.AUDIO CODEC
10.SYSTEM POWER(P1V5)	35.HASWELL_9 (POWER)	60.AUDIO JACK, MIC AMP.
11.SYSTEM POWER(P1V05_M)	36.HASWELL_10 (POWER)	61.CARD READER
12.SYSTEM POWER(P1V05S)	37.HASWELL_11 (GND)	62.BUTTON, LED
13.USB HUB POWER (P1V2A)	38.SYSTEM MEMORY (DIMM0)	63.SMART CARD DAUGHTER BOARD
14.SYSTEM POWER(PVCORE&PVAXG-1)	39.SYSTEM MEMORY (DIMM1)	64.CRT DB WTB CONN
15.SYSTEM POWER(PVCORE&PVAXG-2)	40.EMPTY	65.MIC DB
16.POWER SEQUENCE (SLEEP)	41.EMPTY	66.SCREW
17.DC JACK & BATTERT CONN.	42.DP TO VGA CONVERTER	67.EMPTY
18.HP_OCP	43.DISPLAY PORT	68.MARS-1
19.PVCORE_DGPU	44.LCM & WEBCAM CONN	69.MARS-2
20.PVDDCI	45.SATA HDD, MSATA	70.MARS-3
21.P1V35S_DGPU	46.USB HUB	71.MARS-4
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		77.VRAM-2

INVENTEC			
TITLE			
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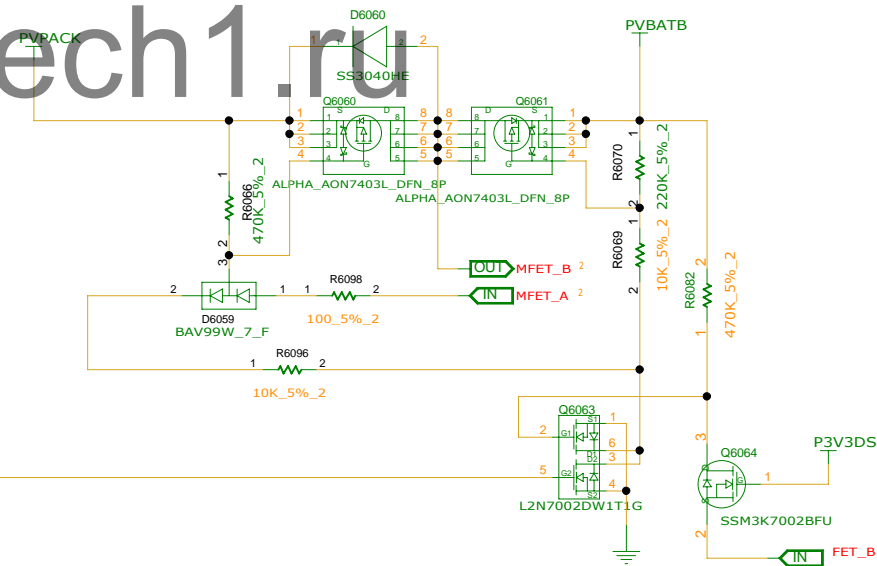
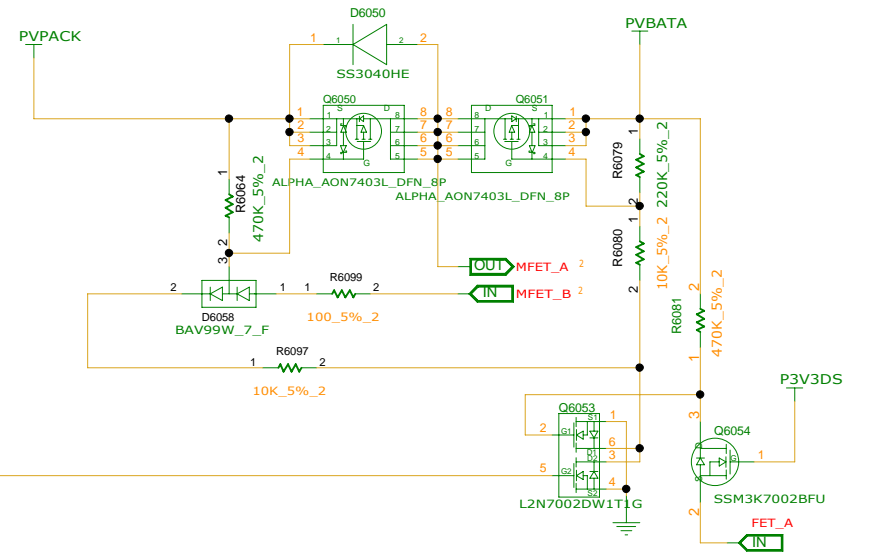
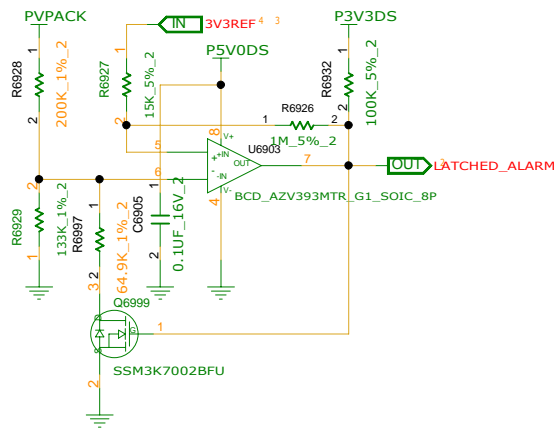
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INVENTEC			
TITLE			
MODEL PROJECT FUNCTION BLOCK DIAGRAM POWER			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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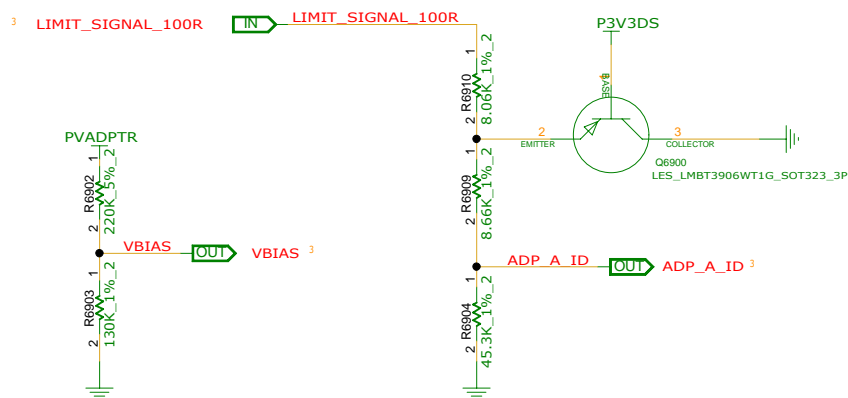
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION

SIZE CODE DOC NUMBER REV
A3 CS 1310xxxxx-0-0 X01

CHANGE by XXX DATE 21-OCT-2002

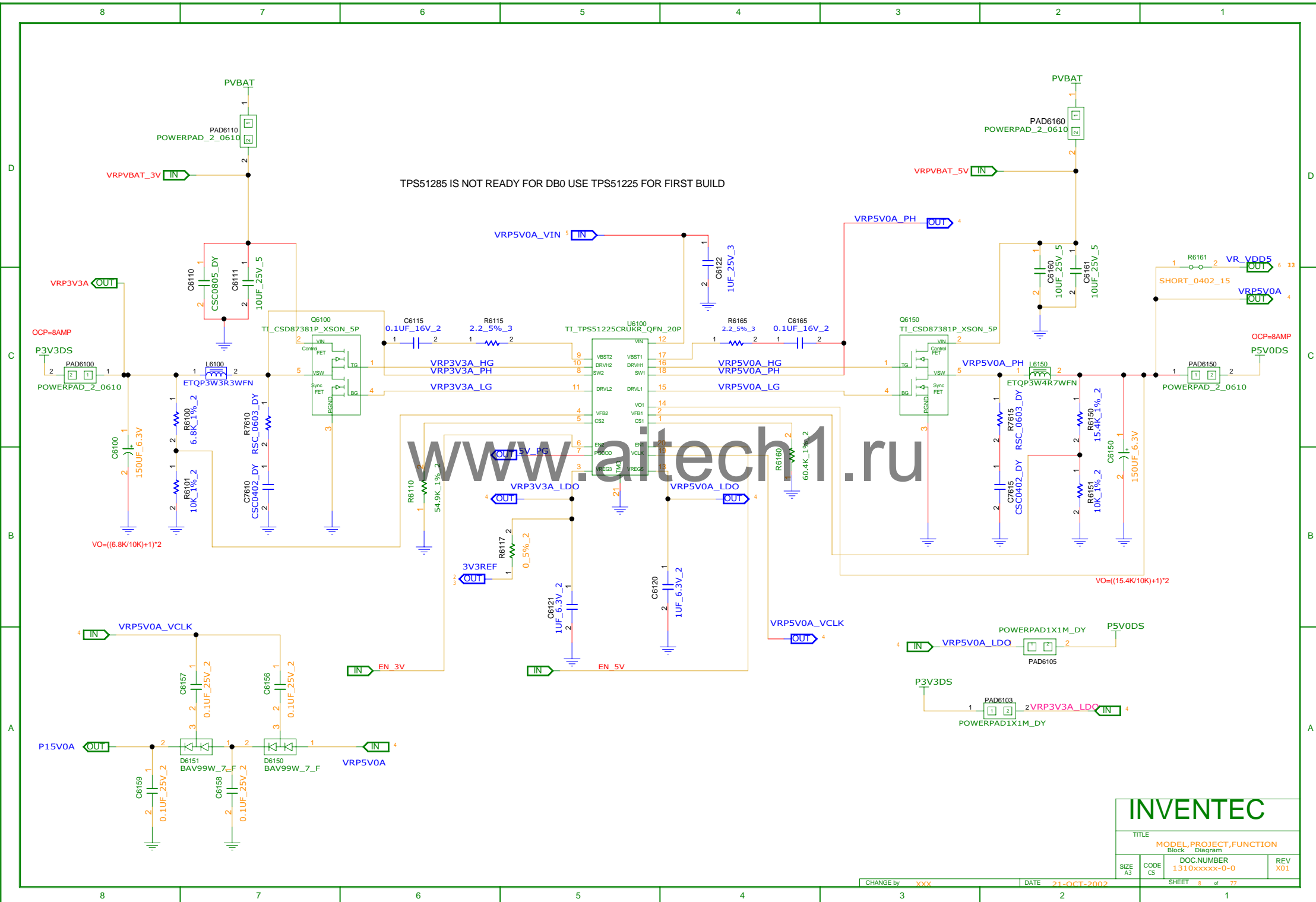
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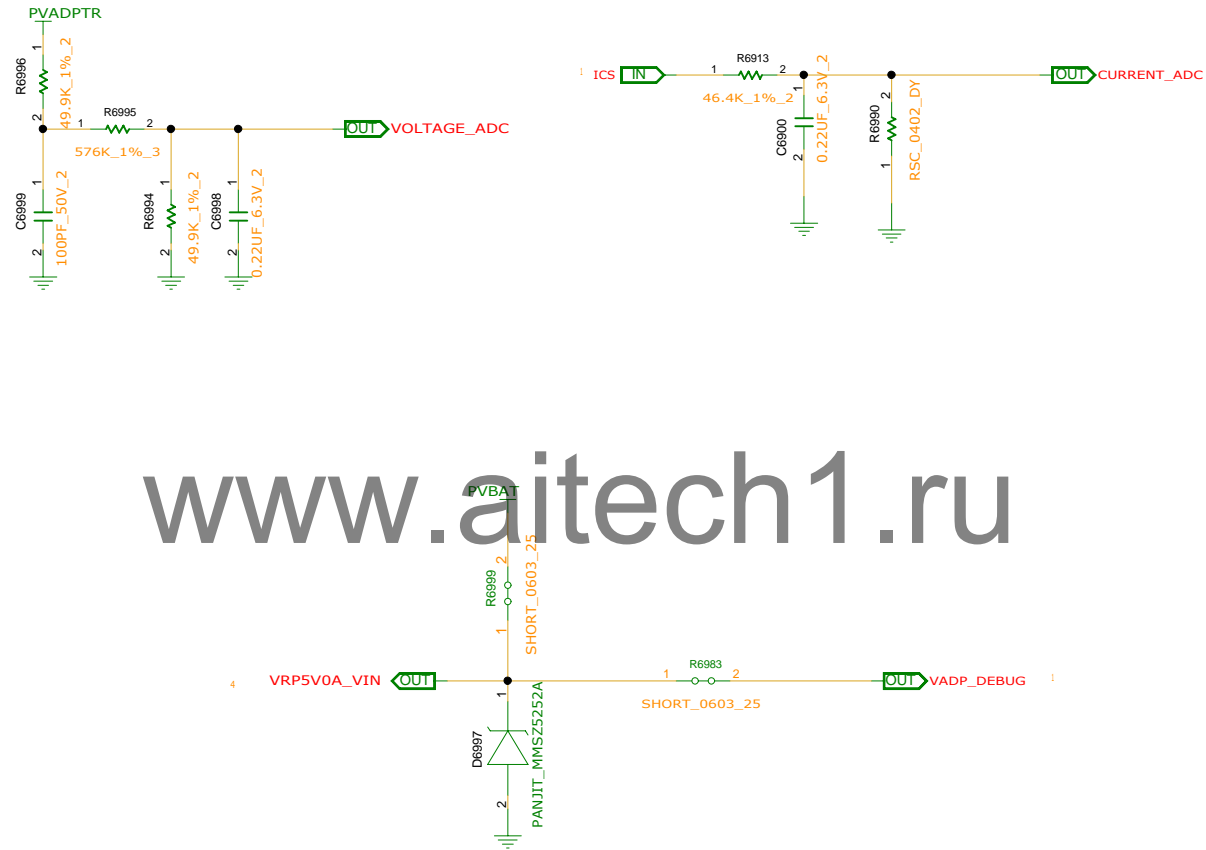


INVENTEC

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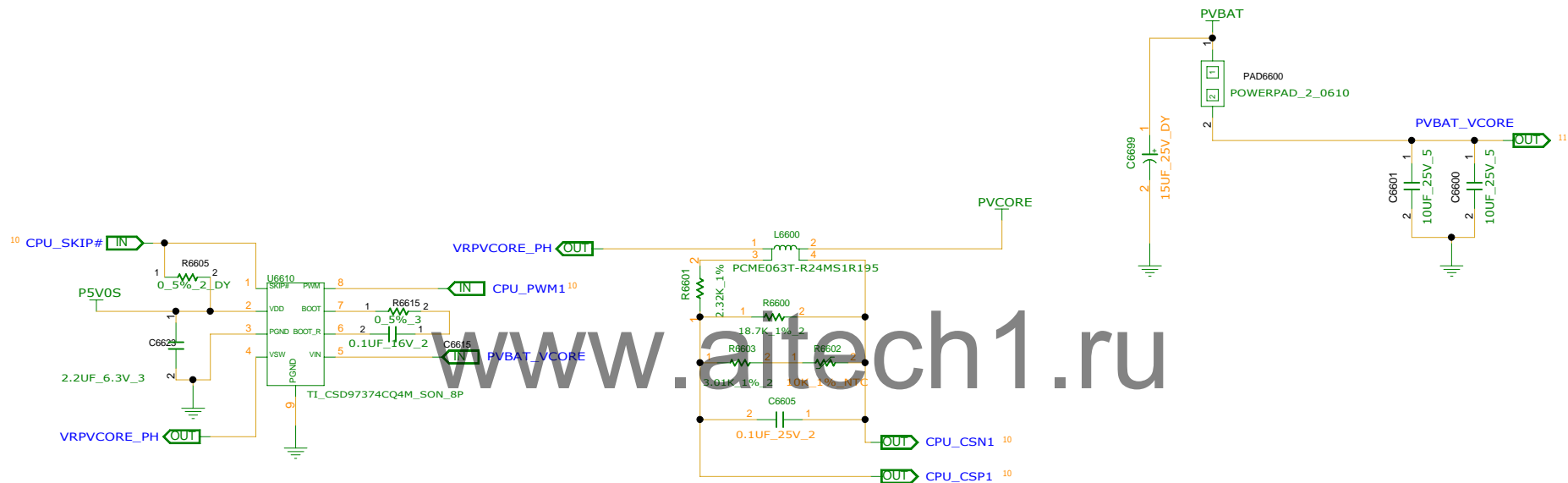


INVENTEC

TITLE			
MODEL, PROJECT, FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

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INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE CODE DOC NUMBER REV
A3 CS 1310xxxxx-0-0 X01

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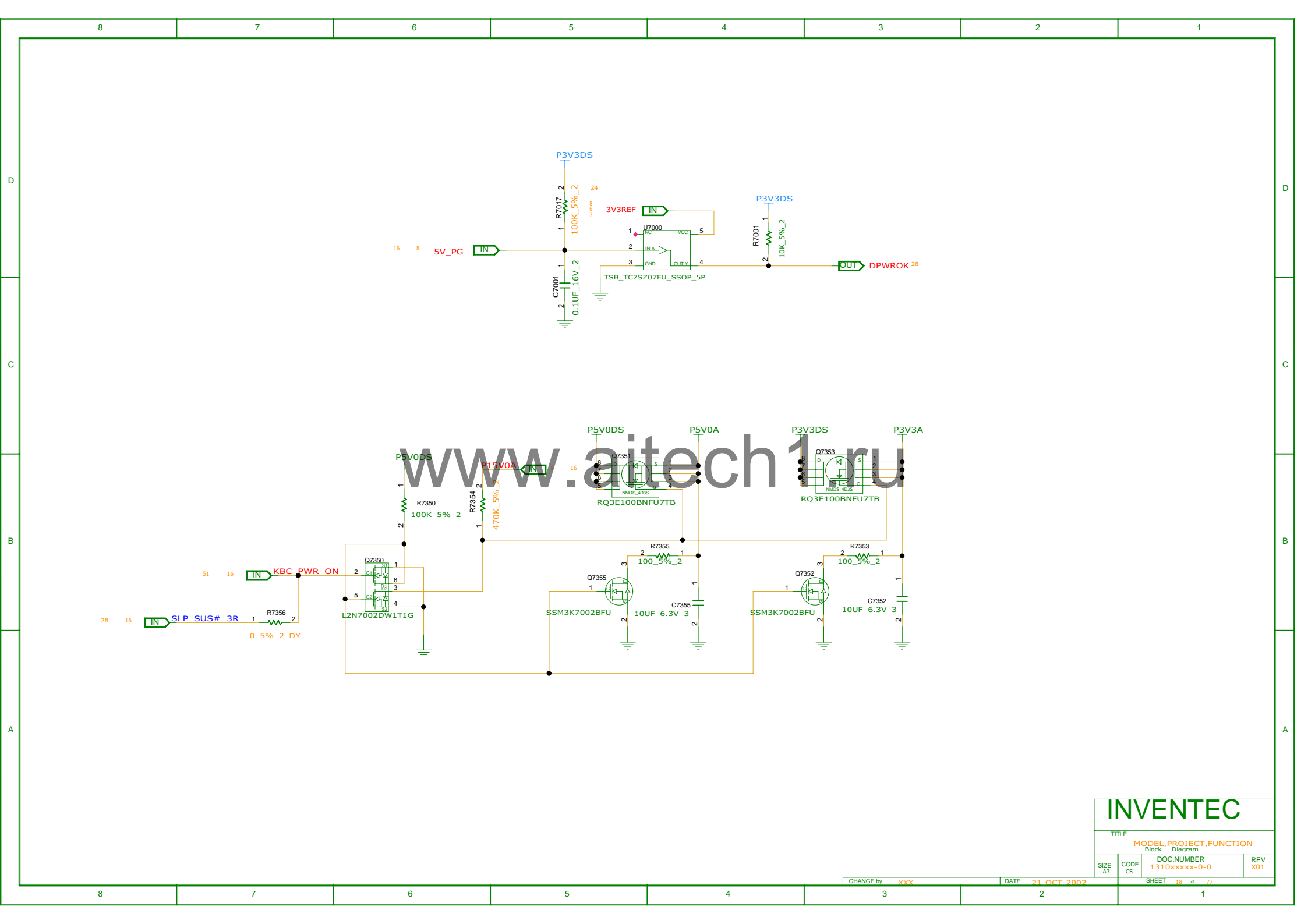
[illegible]

TITLE	MODEL PROJECT FUNCTION
1. <u>Project Description</u>	1. <u>Project Description</u>
2. <u>Project Objectives</u>	2. <u>Project Objectives</u>
3. <u>Project Scope</u>	3. <u>Project Scope</u>
4. <u>Project Organization</u>	4. <u>Project Organization</u>
5. <u>Project Schedule</u>	5. <u>Project Schedule</u>
6. <u>Project Budget</u>	6. <u>Project Budget</u>
7. <u>Project Risks</u>	7. <u>Project Risks</u>
8. <u>Project Communication</u>	8. <u>Project Communication</u>
9. <u>Project Monitoring and Control</u>	9. <u>Project Monitoring and Control</u>
10. <u>Project Closure</u>	10. <u>Project Closure</u>

Block		Diagram	
		DOC NUMBER	

SIZE A3	CODE CS	1310xxxx-0-0
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TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

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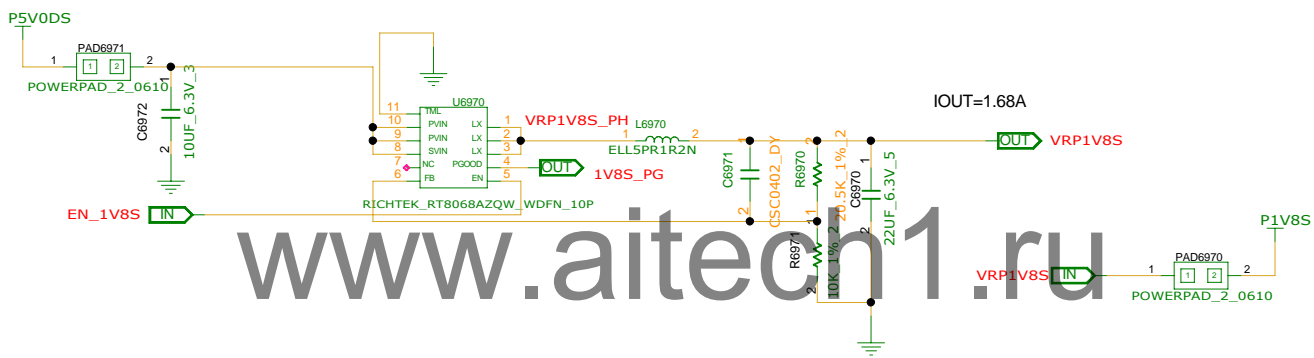
	8	7	6	5	4	3	2	1
D								
C								
B								
A								
	8	7	6	5	4	3	2	1

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EMPTY

INVENTEC

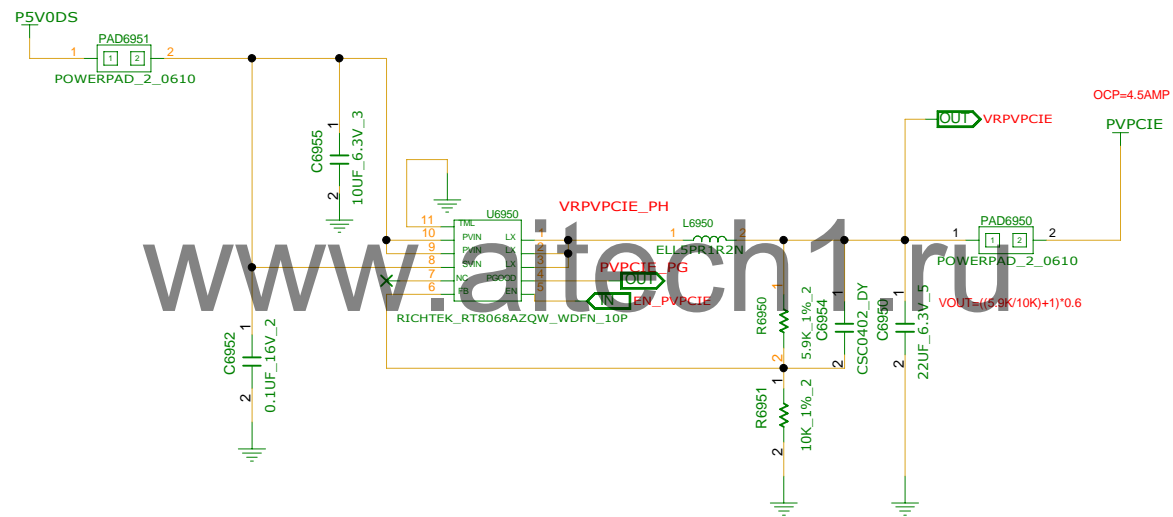
TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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VREF=0.6V
15K=1.5V
10K=1.2V
20.5K=1.83V
MODE=FLOAT=SKIP MODE
MODE=VIN=FCCM MODE

INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
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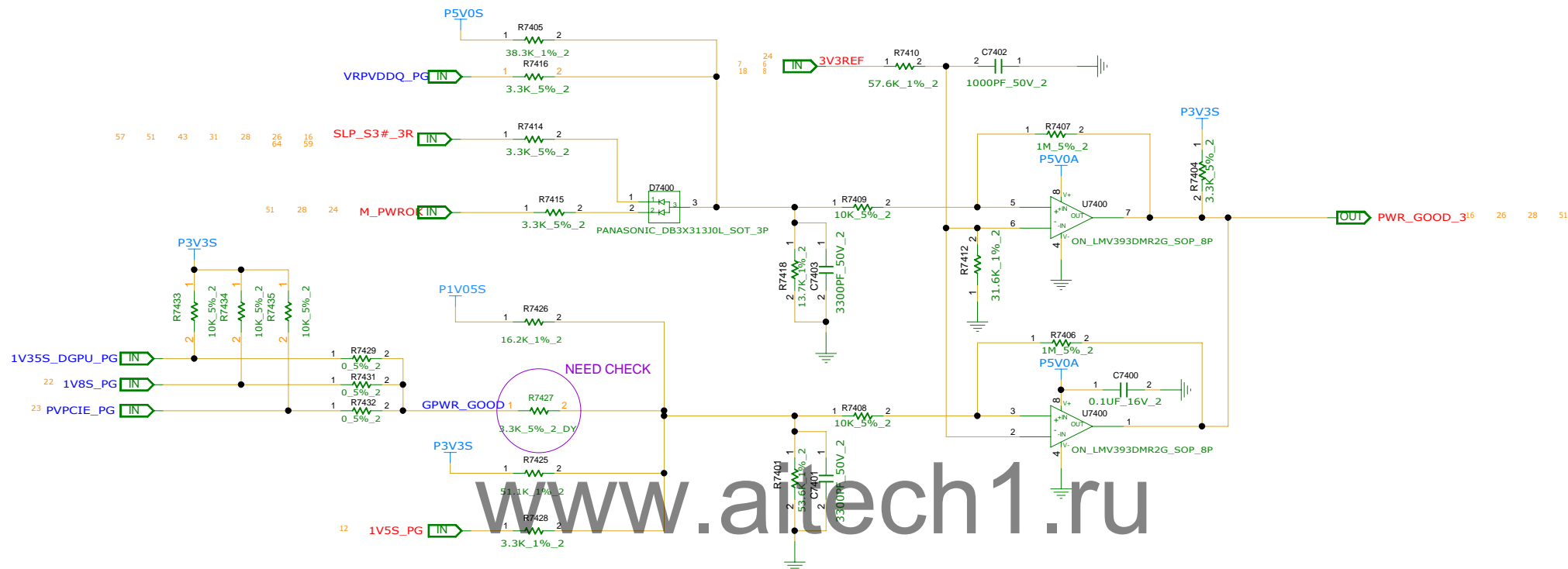
INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01

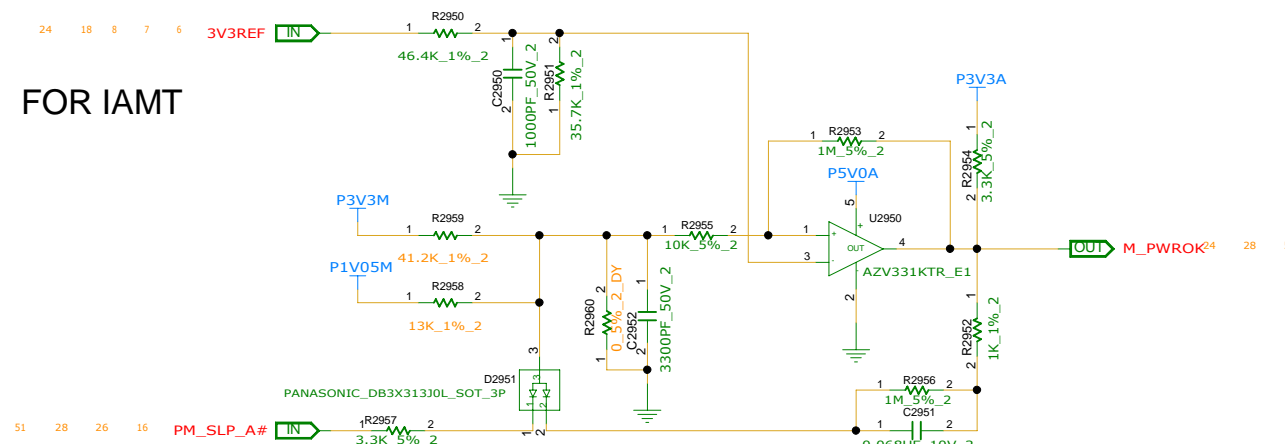
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REFERENCE NUMER : 7400~7450



FOR IAMT



REFERENCE NUMER : 2950~2999

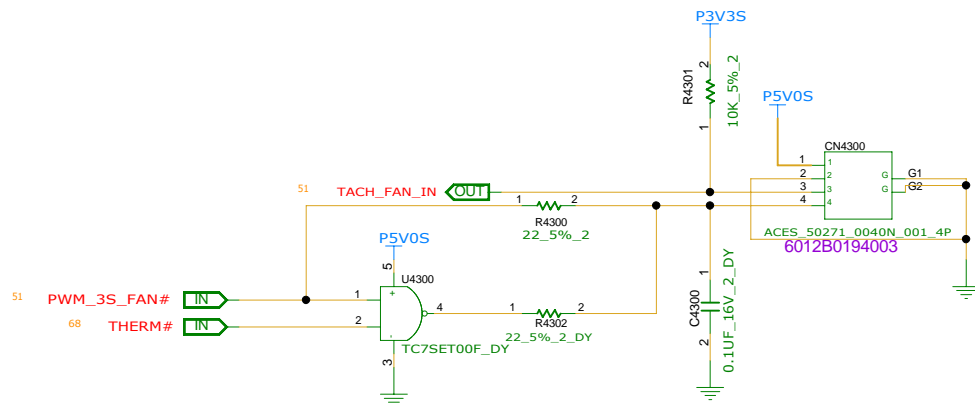
INVENTEC

TITLE			
MODEL PROJECT FUNCTION POWER (SEQUENCE)			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01

CHANGE by XXX DATE 21-OCT-2002

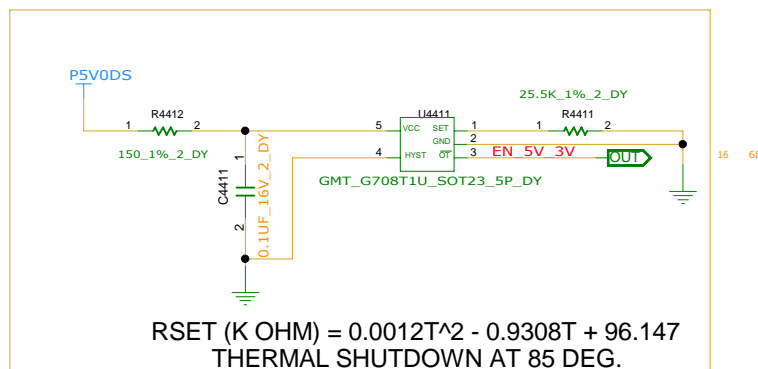
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REFERENCE NUMBER:4400~4349



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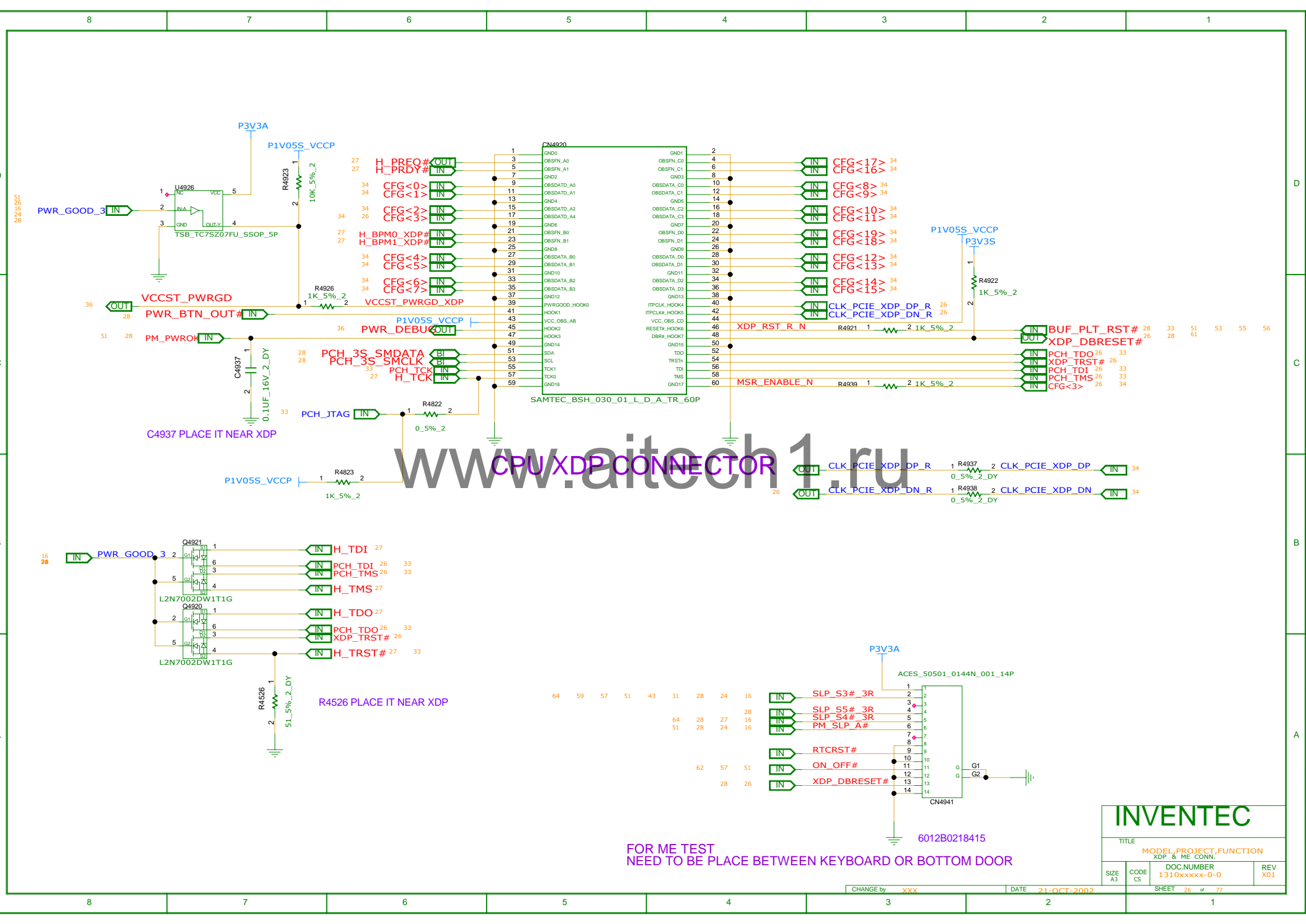
AMBIENT TEMP SENSE
WILL BE NOT USED IN 2013?



REFERENCE NUMBER:4411~4449

INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
FAN & THERMAL			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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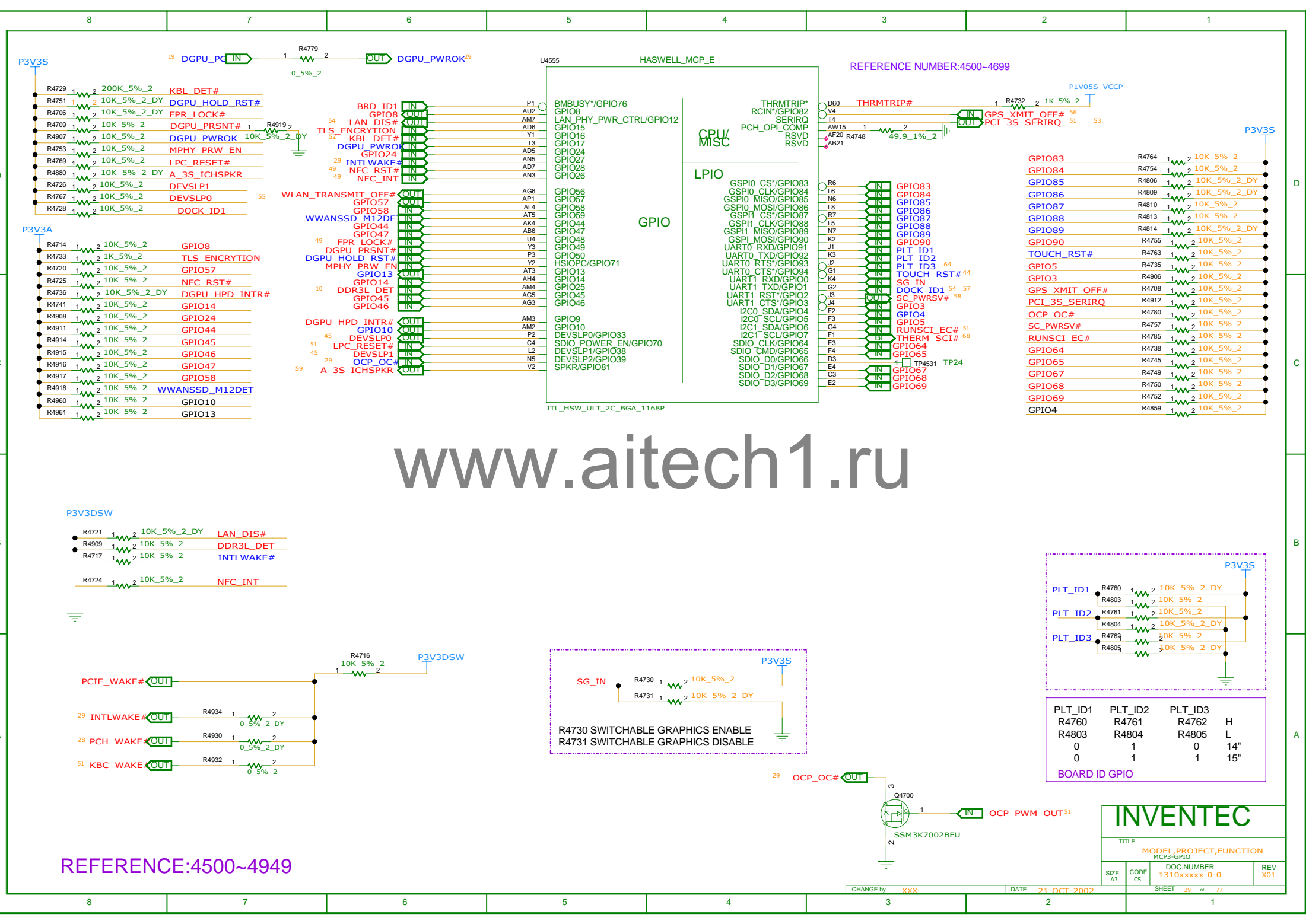
CPU XDP CONNECTOR

R4526 PLACE IT NEAR XDP

C4937 PLACE IT NEAR XDP

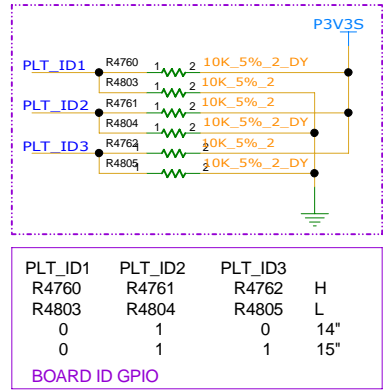
FOR ME TEST
NEED TO BE PLACE BETWEEN KEYBOARD OR BOTTOM DOOR

INVENTEC			
TITLE MODEL PROJECT,FUNCTION XDP & ME CORN			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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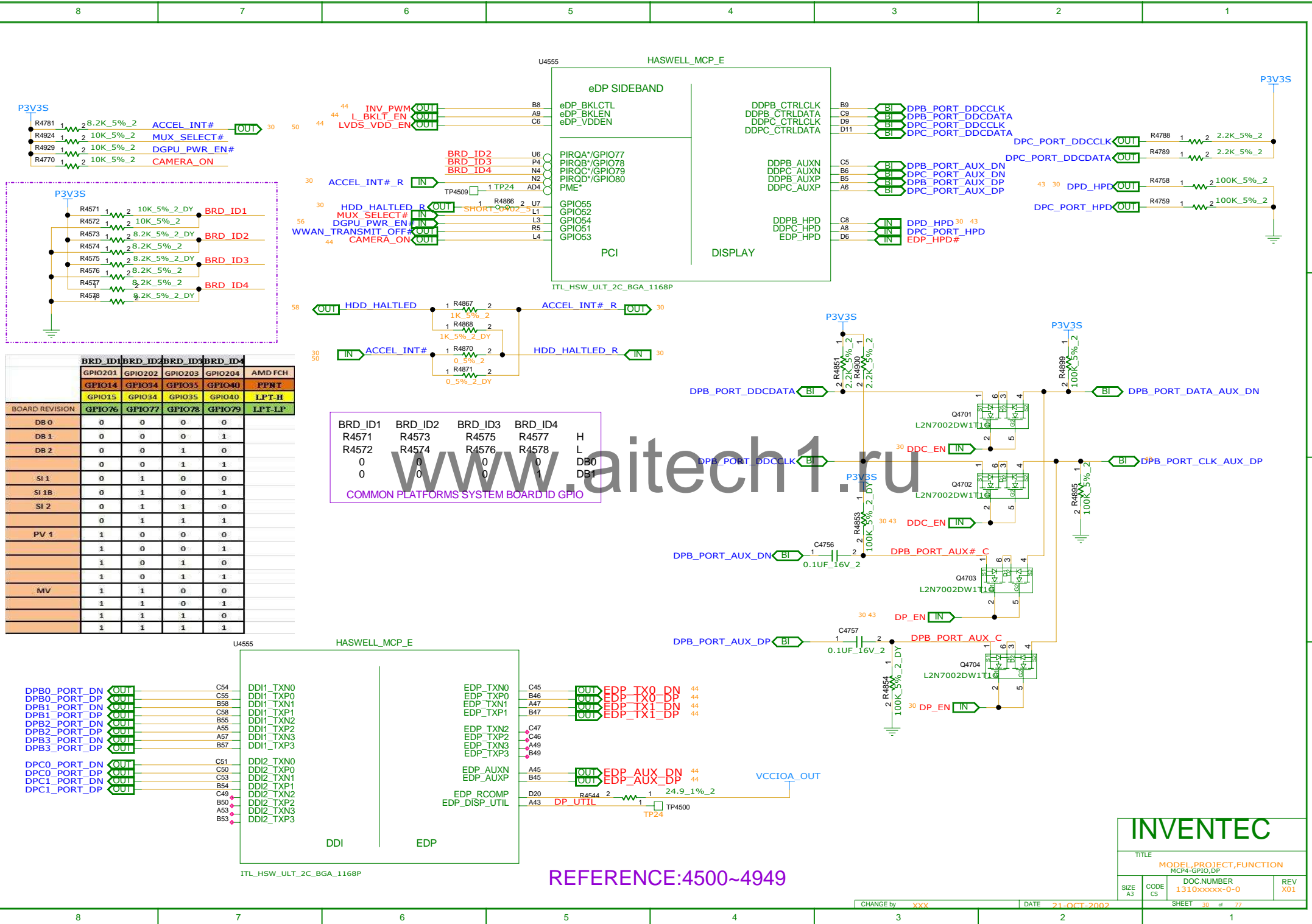


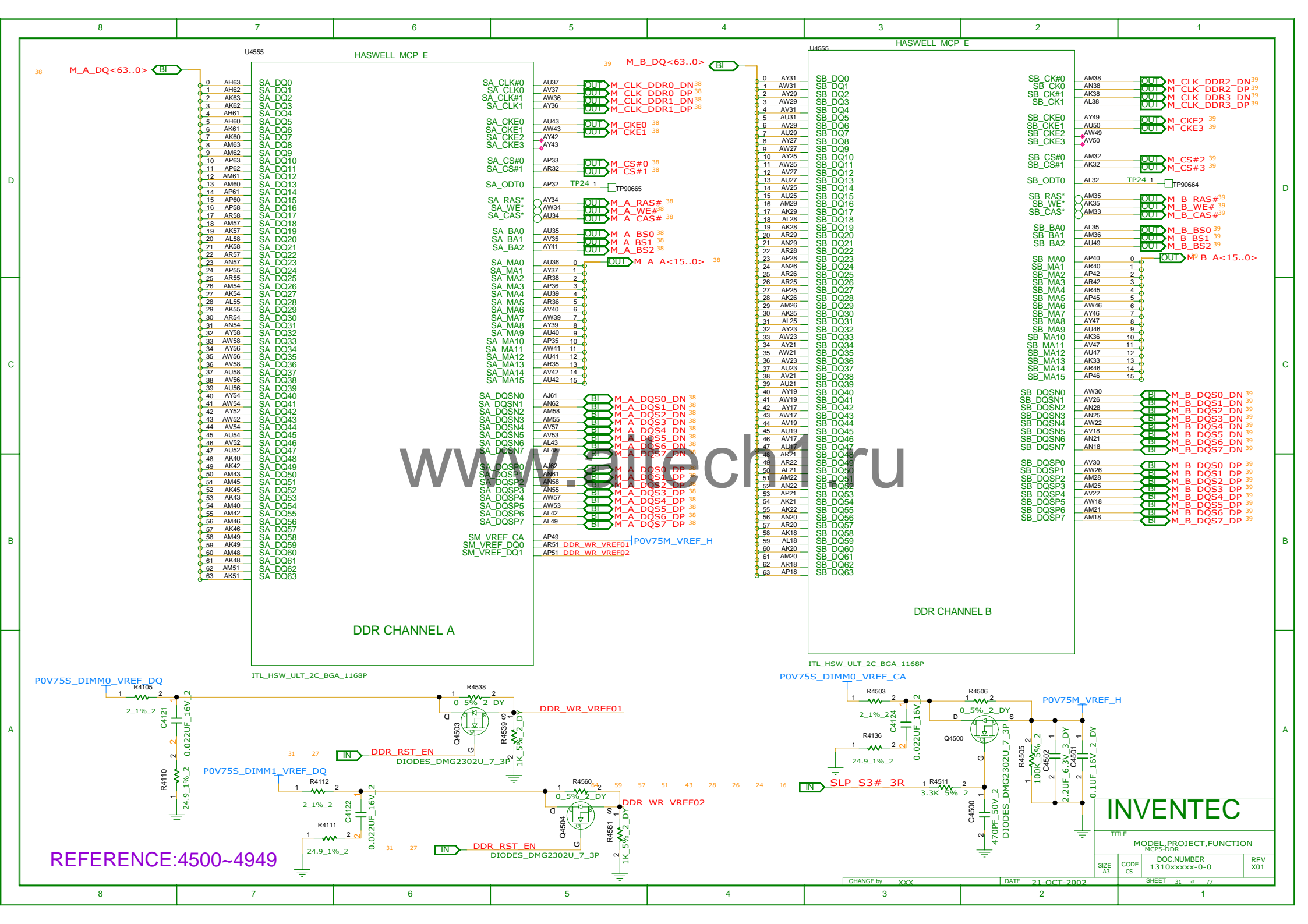
www.aitech1.ru

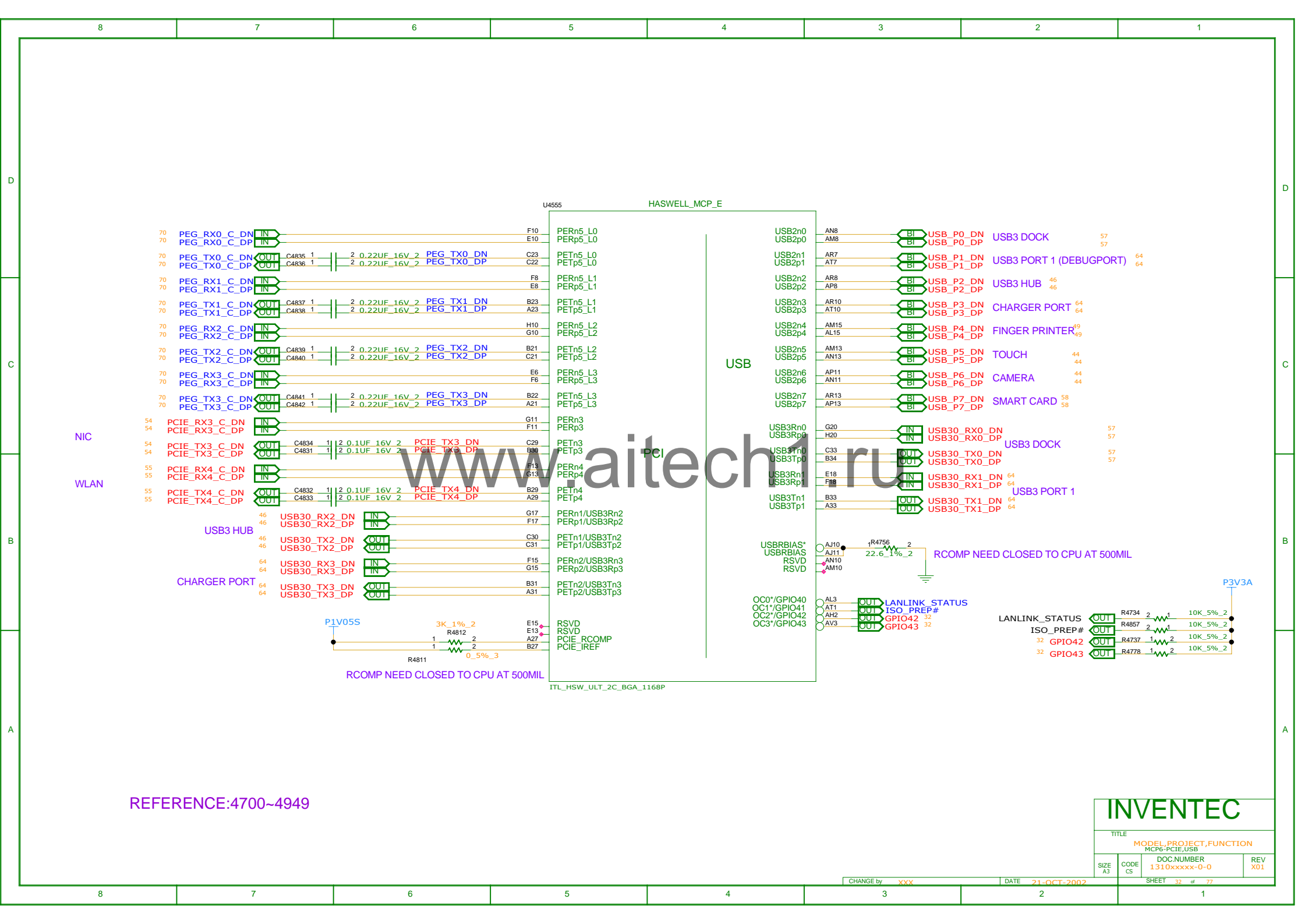
REFERENCE:4500~4949

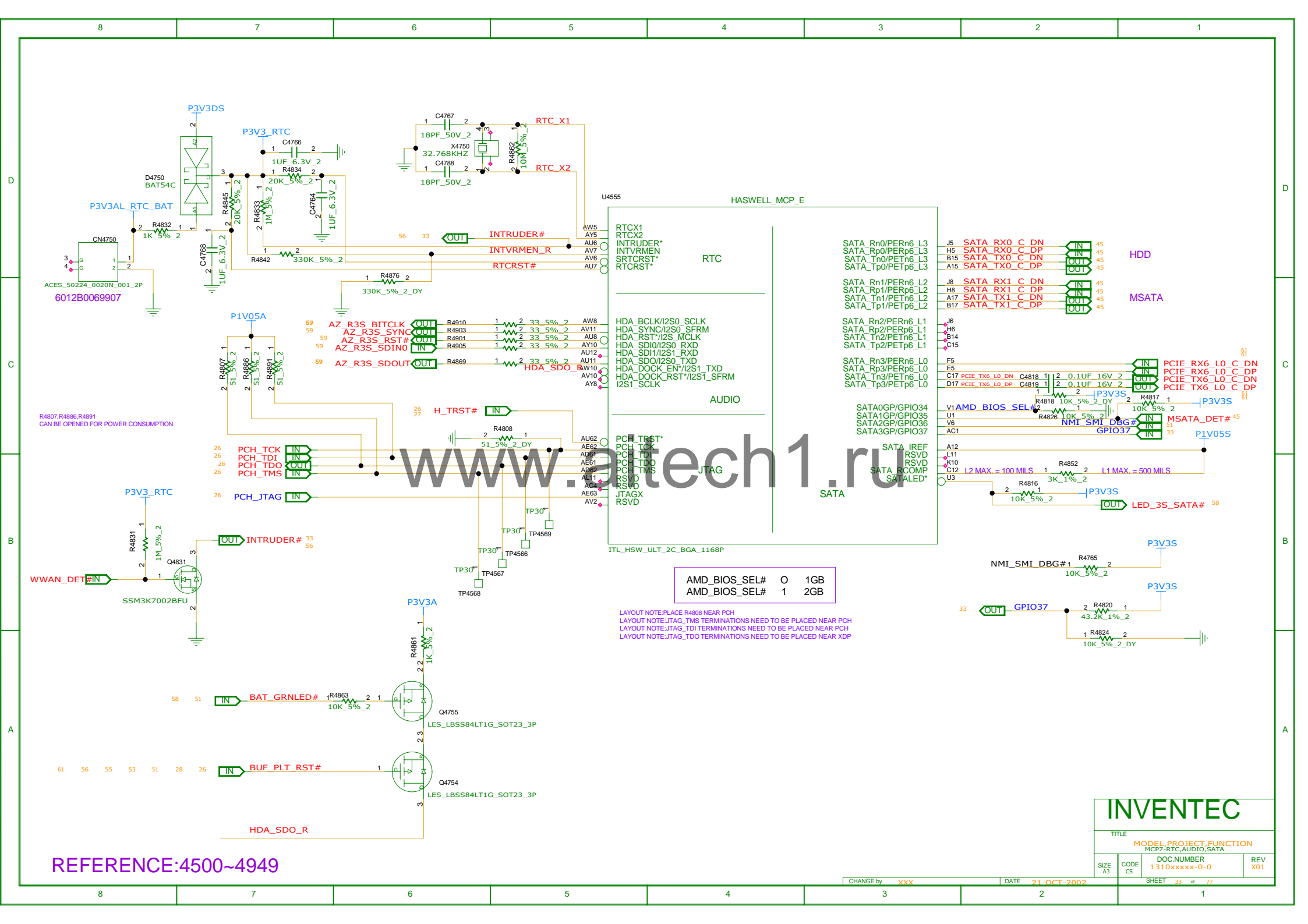


INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
MCP3-GPIO			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01









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6012B0069907

R4807,R4886,R4891
CAN BE OPENED FOR POWER CONSUMPTION

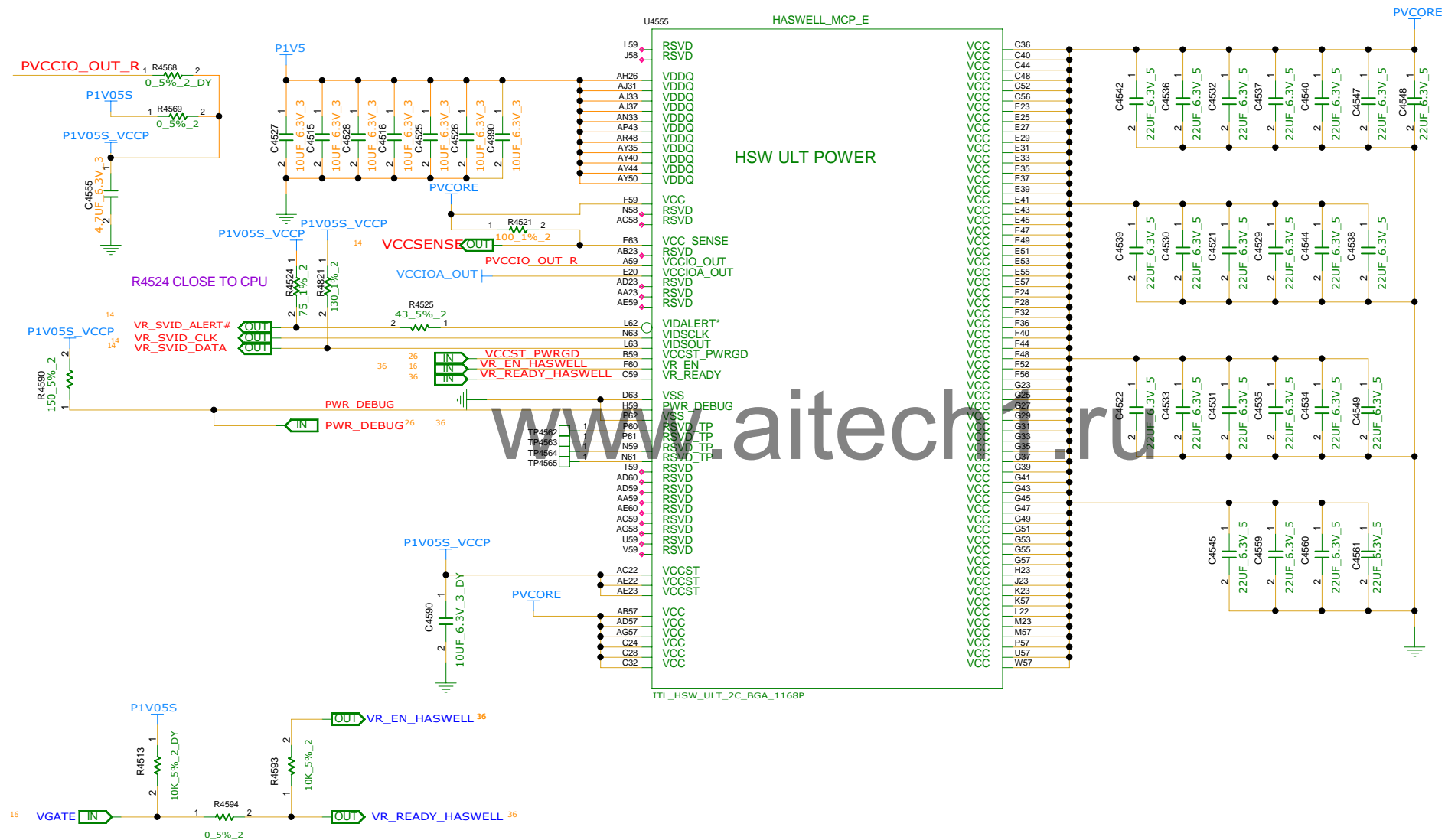
AMD_BIOS_SEL# 0 1GB
AMD_BIOS_SEL# 1 2GB

LAYOUT NOTE:PLACE R4808 NEAR PCH
LAYOUT NOTE:JTAG_TMS TERMINATIONS NEED TO BE PLACED NEAR PCH
LAYOUT NOTE:JTAG_TDI TERMINATIONS NEED TO BE PLACED NEAR PCH
LAYOUT NOTE:JTAG_TDO TERMINATIONS NEED TO BE PLACED NEAR PCH

REFERENCE:4500~4949

INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
MCP7-RTC-AUDIO-SATA			
SIZE		DOC NUMBER	
A3		1310xxxx-0-0	
CODE		REV	
CS		X01	
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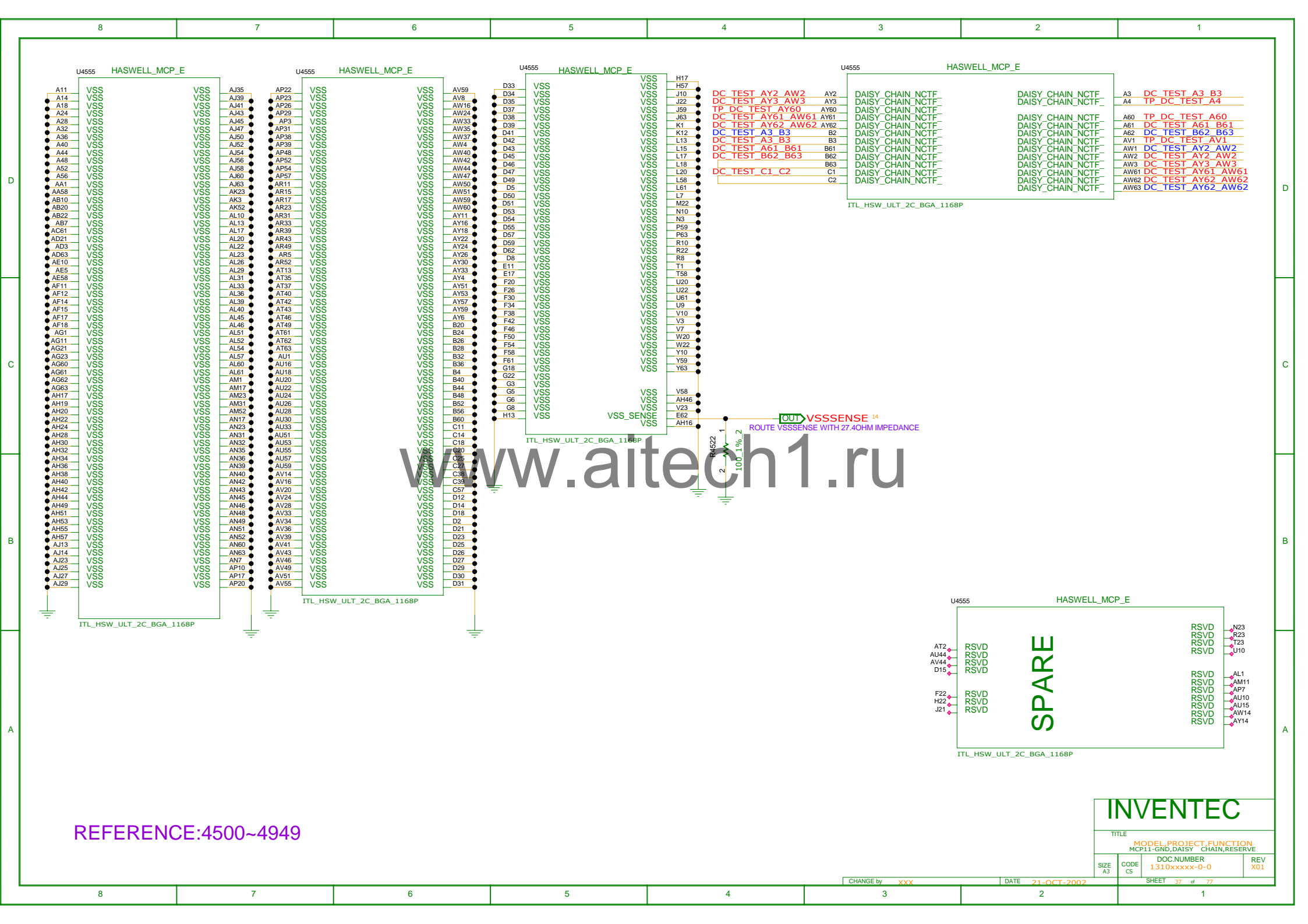
ROUTE VCCSENSE WITH 27.4OHM IMPEDANCE

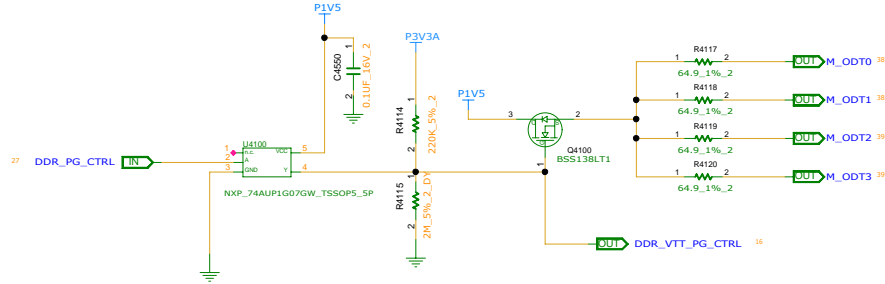


REFERENCE:4500~4949

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION MCP10-POWER			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
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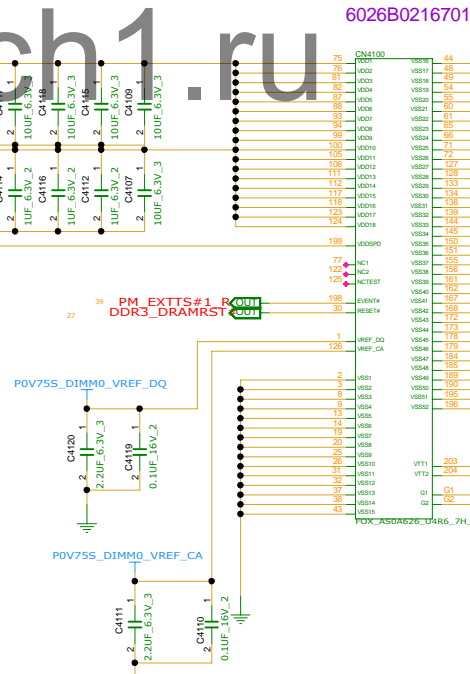
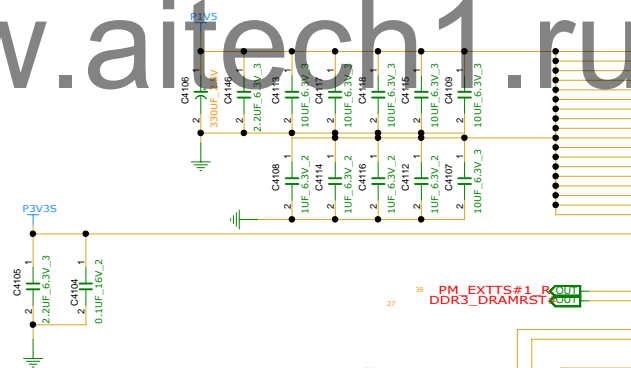
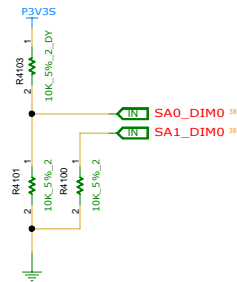




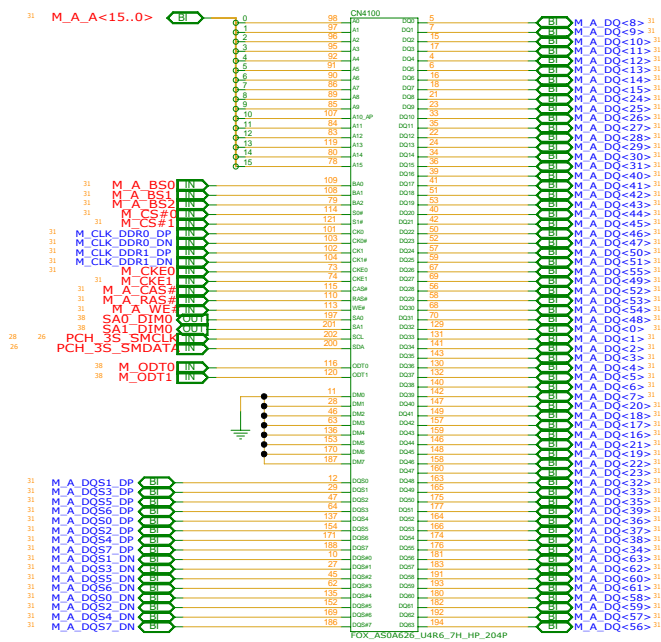
NOTE:

IF SA0_DIMM0=0, SA1_DIMM0=0
SO-DIMMA SPD ADDRESS IS 0XA0
SO-DIMMA TS ADDRESS IS 0X30

IF SA0_DIMM0=1, SA1_DIMM0=0
SO-DIMMA SPD ADDRESS IS 0XA2
SO-DIMMA TS ADDRESS IS 0X32



REFERENCE NUMBER:4100-4299



INVENTEC			
MODEL/PROJECT/FUNCTION			
DDR3_SO-DIMM0			
SIZE	CODE	DOC NUMBER	REV
1	131000000-0-0	201	01
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TITLE			
MODEL,PROJECT,FUNCTION DDR3_SO-DIMM0			
SIZE C	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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RESERVED

INVENTEC

TITLE

MODEL PROJECT FUNCTION
DPB DEMUX1 TO DP

SIZE
A3

CODE
CS

DOC NUMBER
1310xxxxx-0-0

REV
X01

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RESERVED

INVENTEC

TITLE			
MODEL PROJECT FUNCTION DPC DEMUX2 TO VGA			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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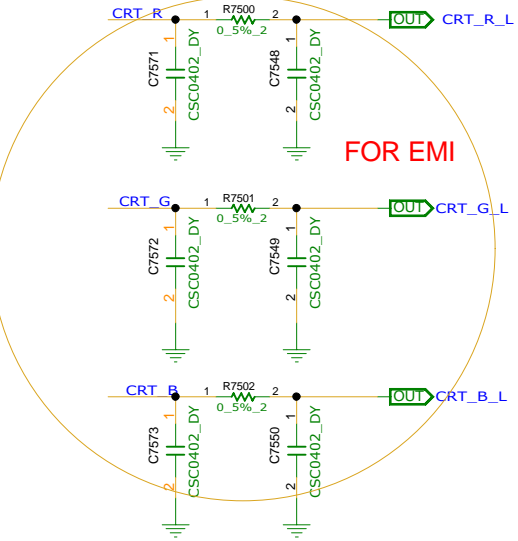
PIN 2	3393 R3062 C3069 C3070	3355 R3061
PIN 5	R3065	R3063 C3077
PIN 8		C3061 C3062
PIN 11	C3073	
PIN 14	R3064 C3074	R3080
PIN 15		R3078
PIN 16		R3067
PIN 17		R3079
PIN 27	L3066 C3087	
PIN 35	C3068	R3081
PIN 36	R3077	
PIN 37	R3074	R3076
PIN 38	10K_DY	R3075 C3090 C3091
PIN 39		L3068 C3092
PIN 40		R3092

42A6 41A1 IN DPC_PORT_CLK_AUX_DP C3075 1 2 0.1UF_16V_2 DPC_PORT_CLK_AUX_C1_DP
42A6 41A1 IN DPC_PORT_DATA_AUX_DN C3076 1 2 0.1UF_16V_2 DPC_PORT_DATA_AUX_C1_DN

41C3 IN DPC0_PORT_DP
41C3 IN DPC0_PORT_DN
41B3 IN DPC1_PORT_DP
41B3 IN DPC1_PORT_DN

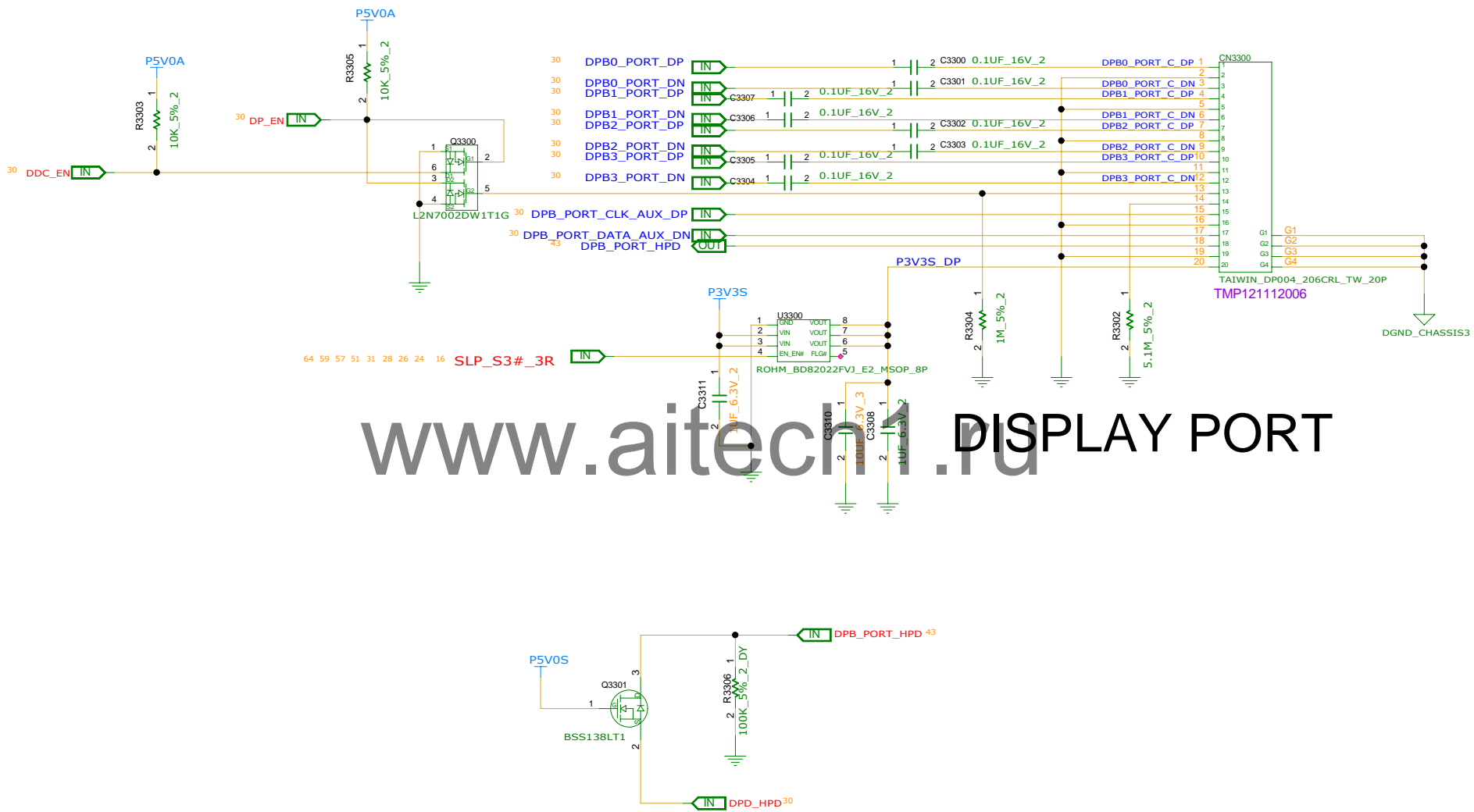
42C8 41A1 IN DPC_PORT_CLK_AUX_DP
42C8 41A1 IN DPC_PORT_DATA_AUX_DN

CHECK IF DEMUX NOT NEED R3079,R3080



INVENTEC

TITLE			
MODEL PROJECT/FUNCTION DP-TO-VGA CONVERTER			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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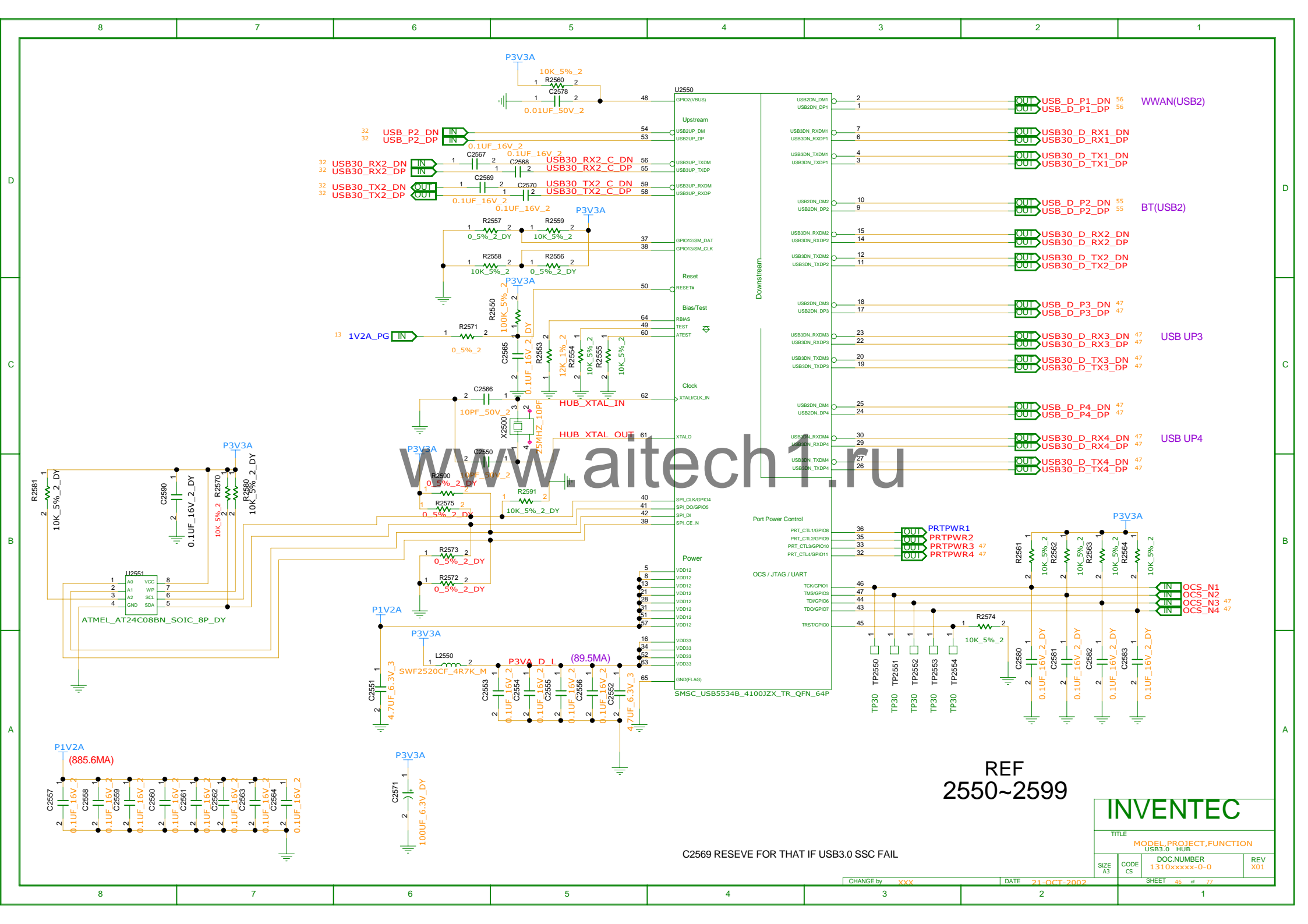


www.aitech.ru DISPLAY PORT

REFERENCE NUMBER:3300~3399

INVENTEC			
TITLE MODEL PROJECT,FUNCTION CRT & DP			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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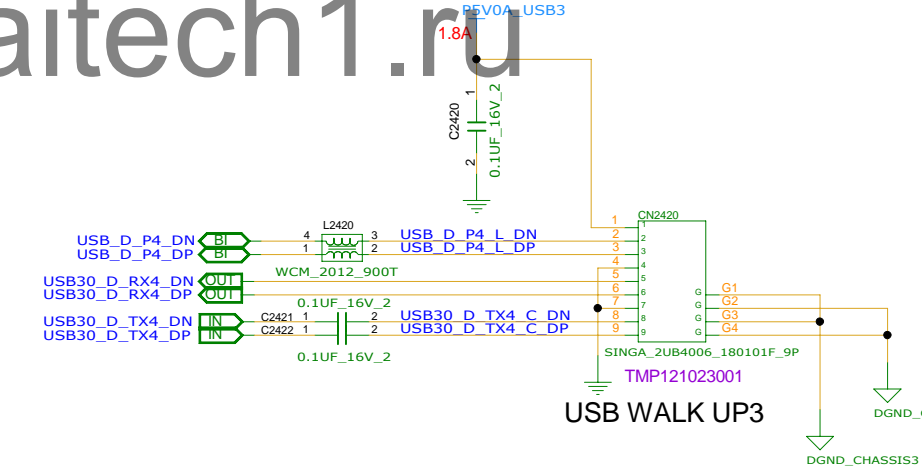
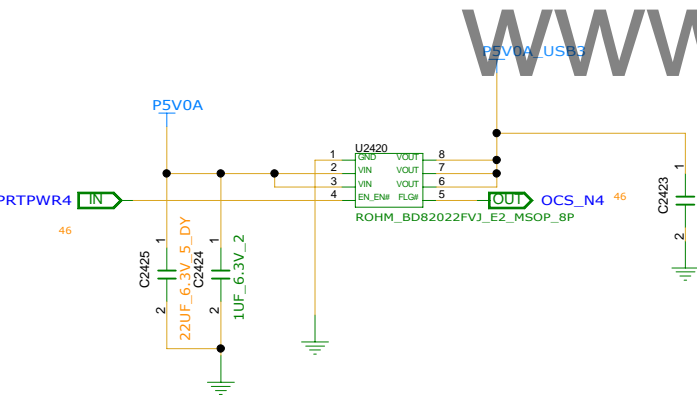
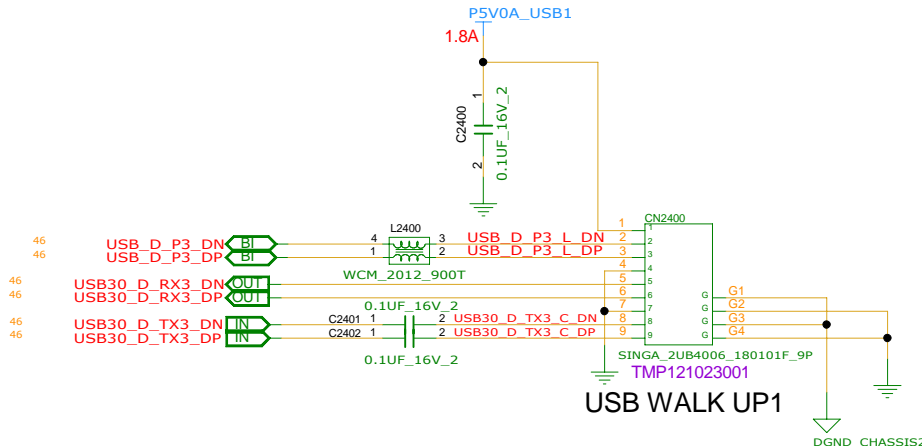
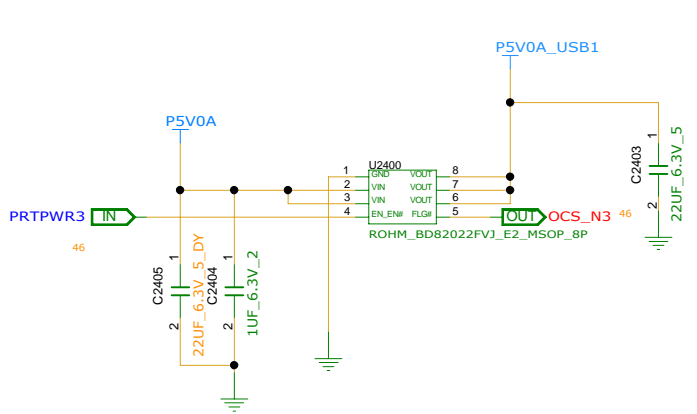


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REF
2550~2599

C2569 RESEVE FOR THAT IF USB3.0 SSC FAIL

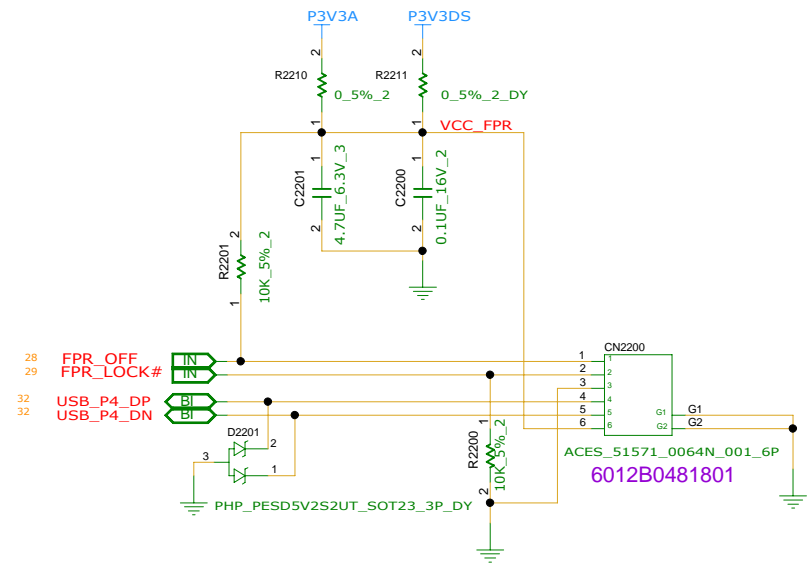
INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
USB3.0 HUB			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01



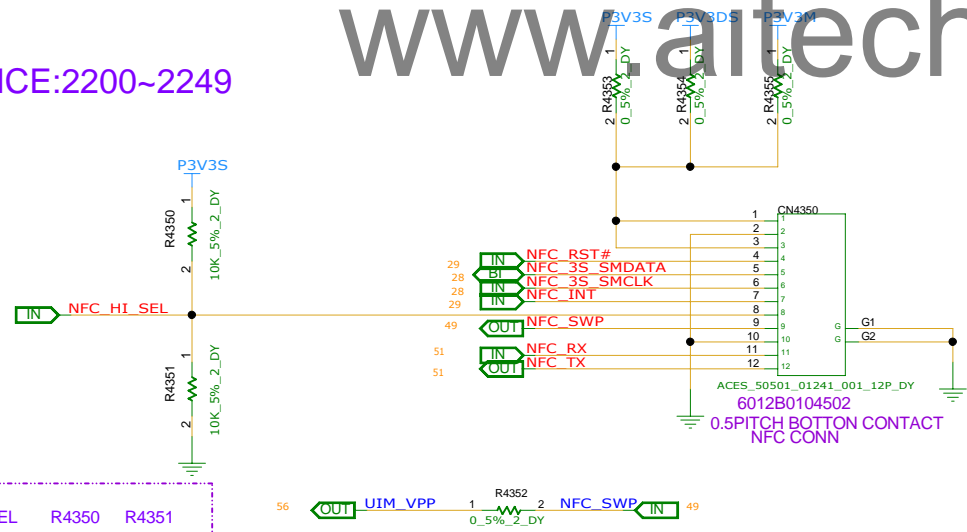
www.aitech1.ru

REFERENCE NUMBER:2400~2499

INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
USB & USB CHARGER			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
CHANGE by XXXX			
DATE 21-OCT-2002			
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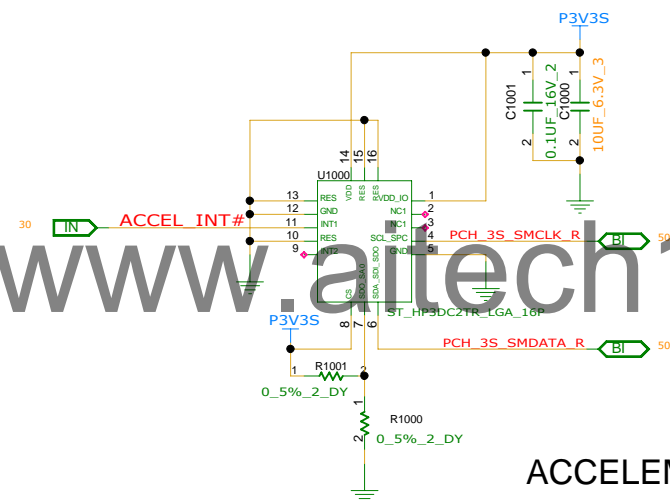
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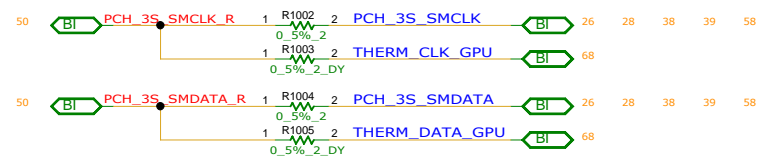
NFC_HI_SEL R4350 R4351
HIGH (UART) INSTALL
LOW (I2C) INSTALL

INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
FINGER PRINTER & NFC			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
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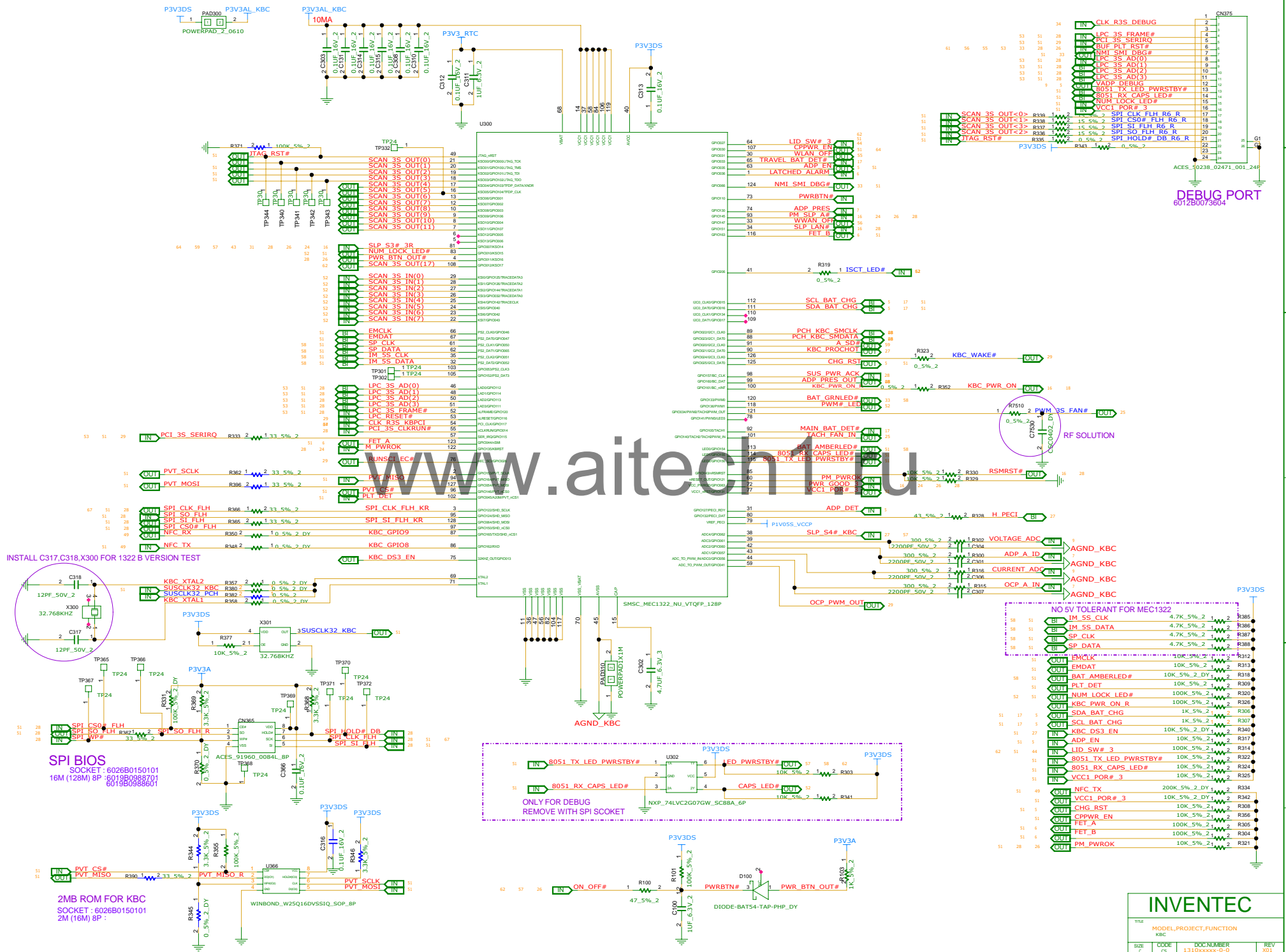
ACCELEMATOR



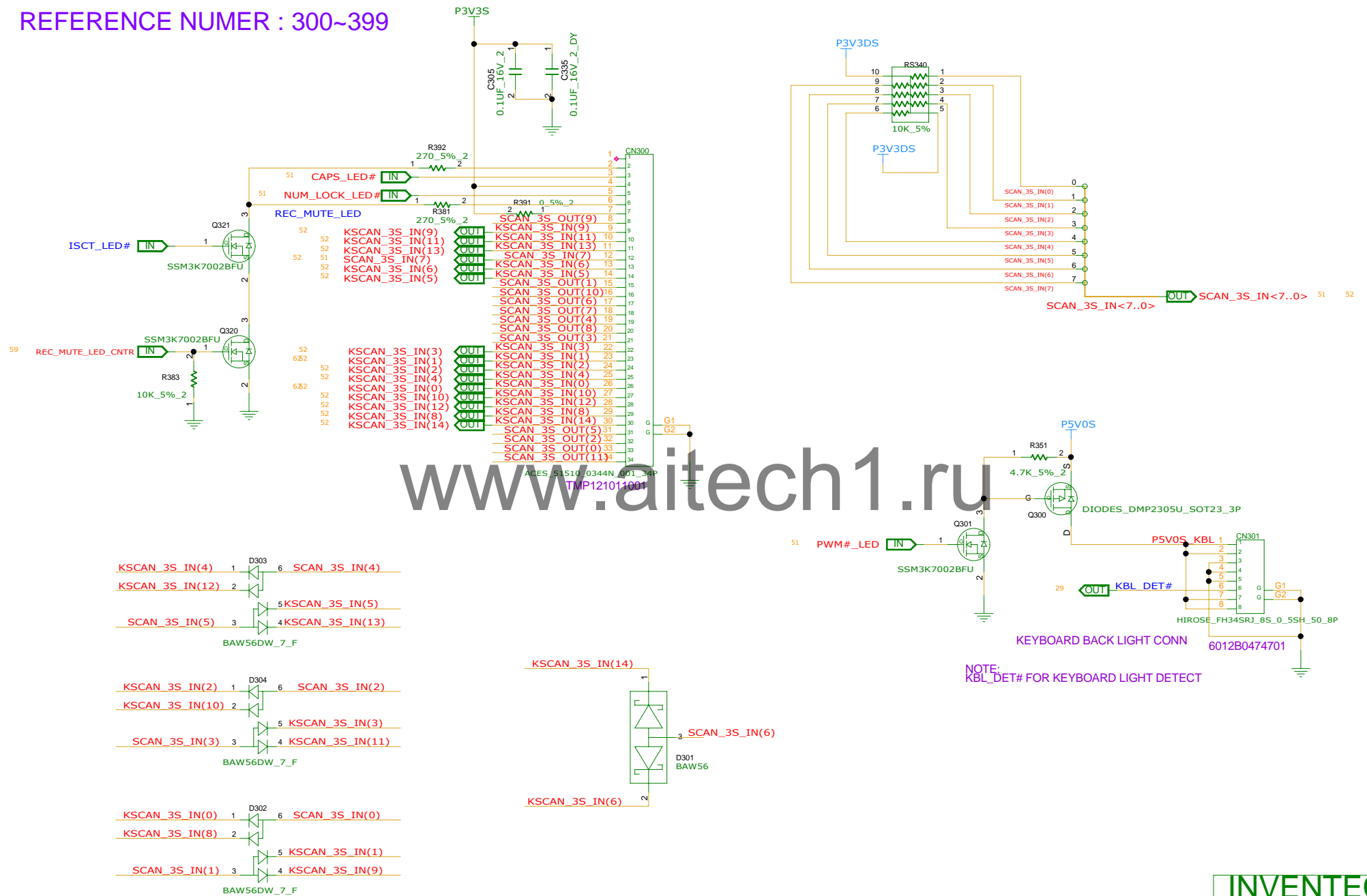
REFERENCE NUMER : 1000~1099

INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
ACCELEMATOR			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
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REFERENCE NUMBER : 300~399

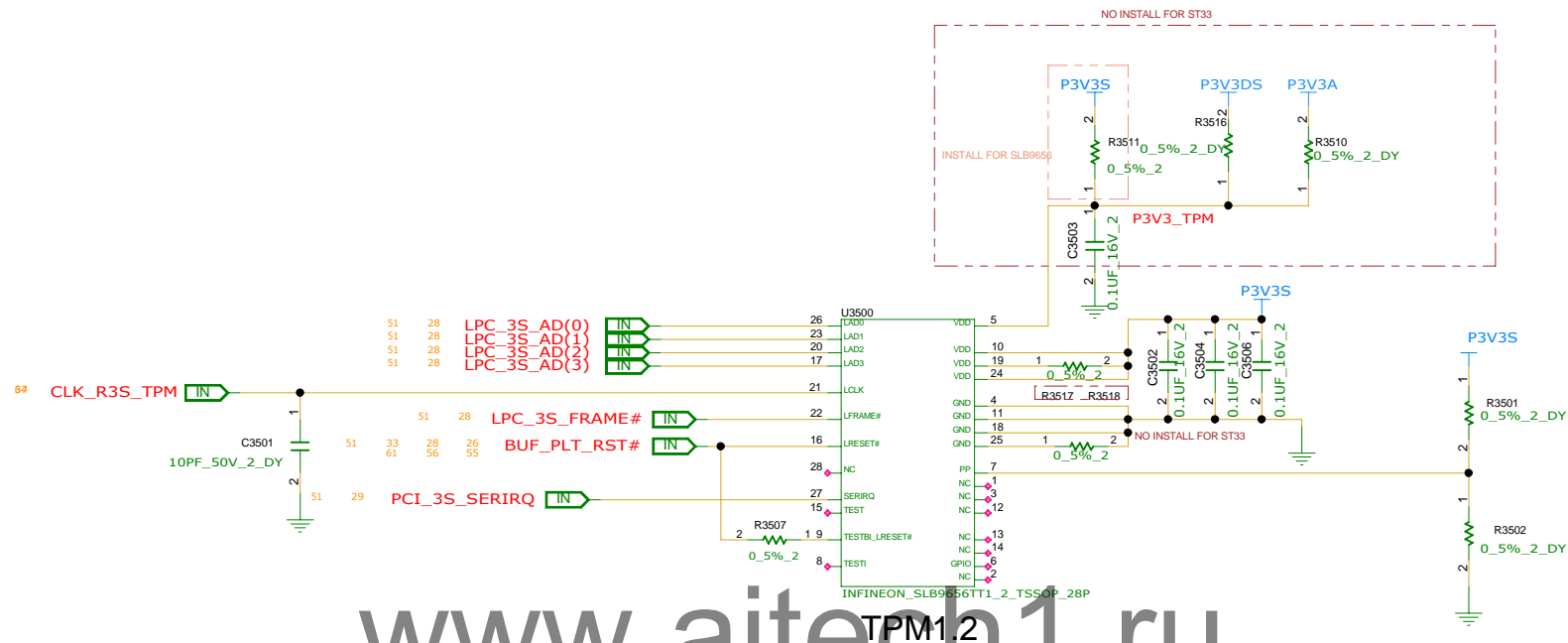


INVENTEC

TITLE
MODEL PROJECT, FUNCTION
KEYBOARD

SIZE A3 CODE CS DOC NUMBER 1310xxxx-0-0 REV X01

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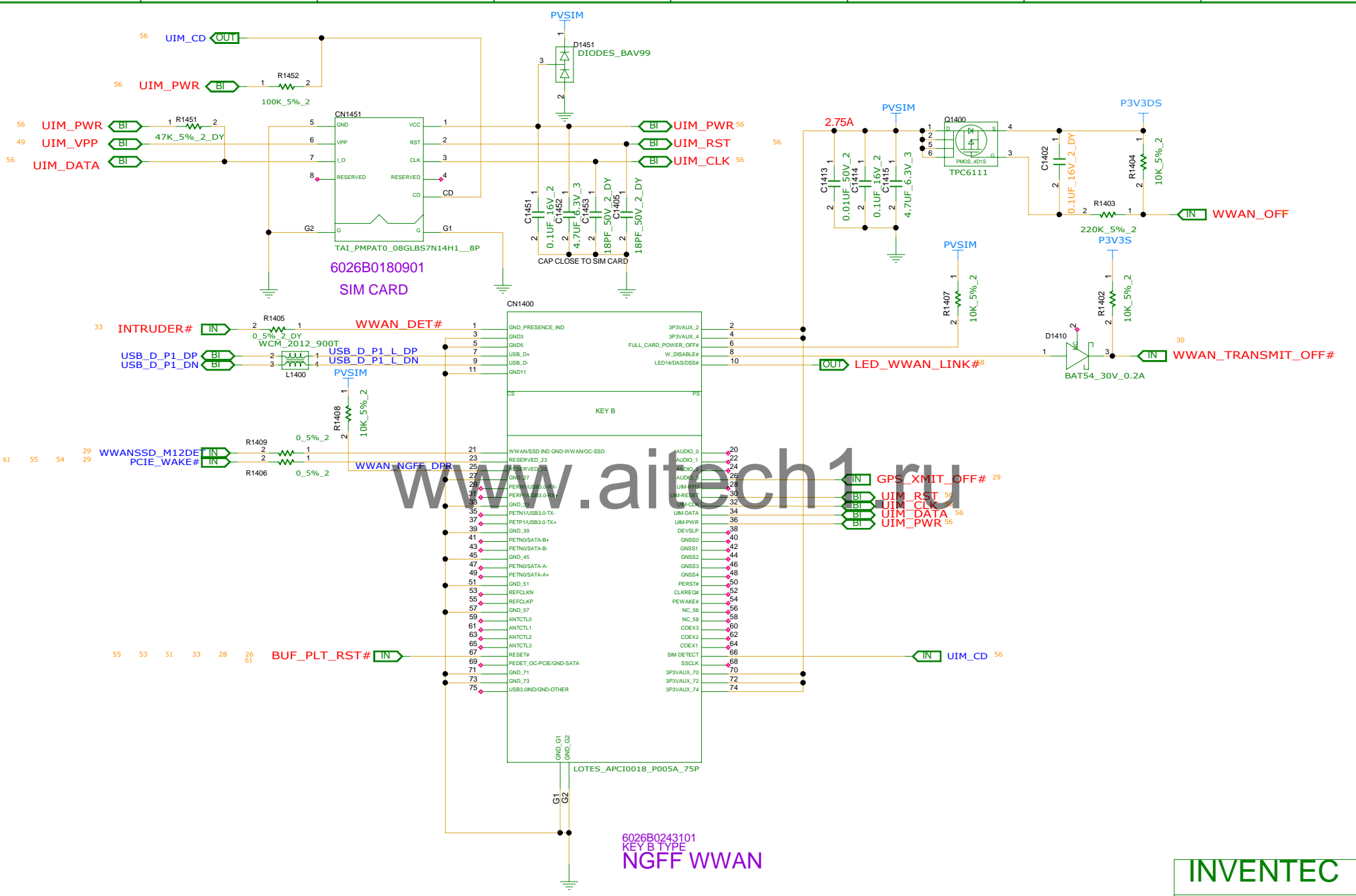
www.aitech1.ru

SLB9656TT1.2		ST33
C3503	INSTALL	OPEN
R3507	INSTALL	OPEN
R3511	INSTALL	OPEN
R3517	INSTALL	OPEN
R3518	INSTALL	OPEN

REFERENCE NUMER : 3500~3549

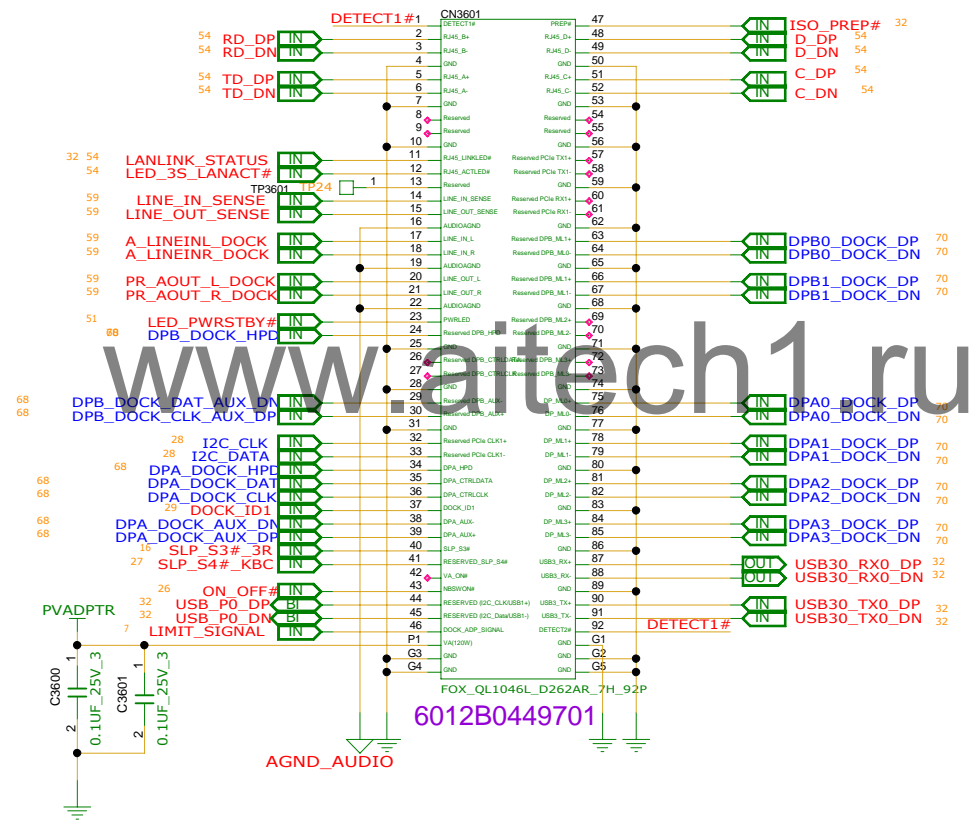
INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
TPM			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 53 of 77			

CHANGE by XXX DATE 21-OCT-2002



REFERENCE NUMBER:1400~1499

INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
WWAN NGFF			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 56 of 77			

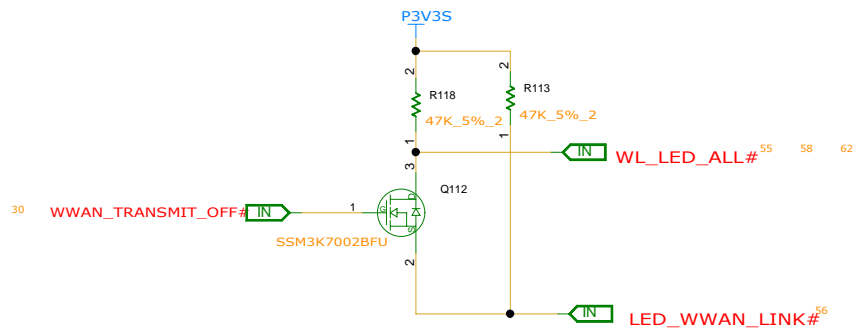


INVENTEC

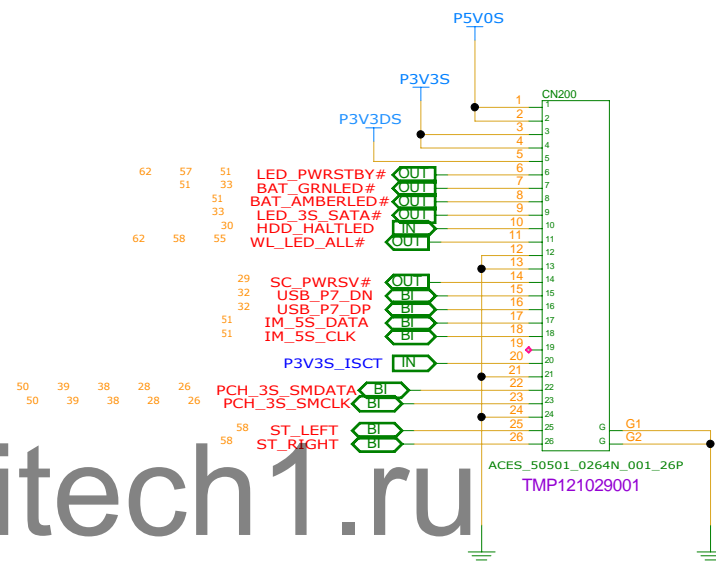
TITLE
MODEL PROJECT,FUNCTION
DOCKING CONN.

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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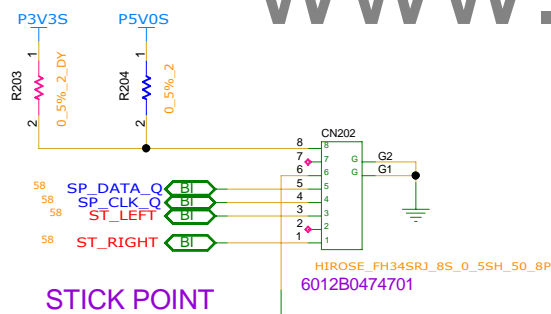
CHANGE by XXX DATE 21-OCT-2002 SHEET 57 of 77



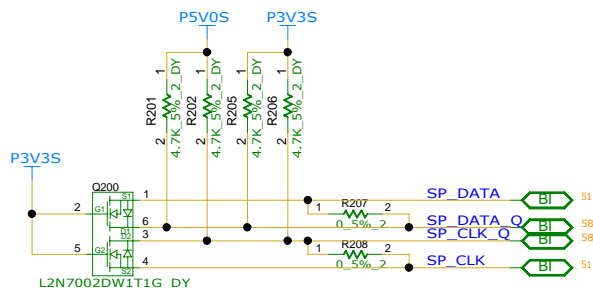
WLAN_WWAN_BLUETOOTH_LED



SMART CARD AND TOUCHPAD D/B W TO B CONN



STICK POINT



STICK POINT OPTION

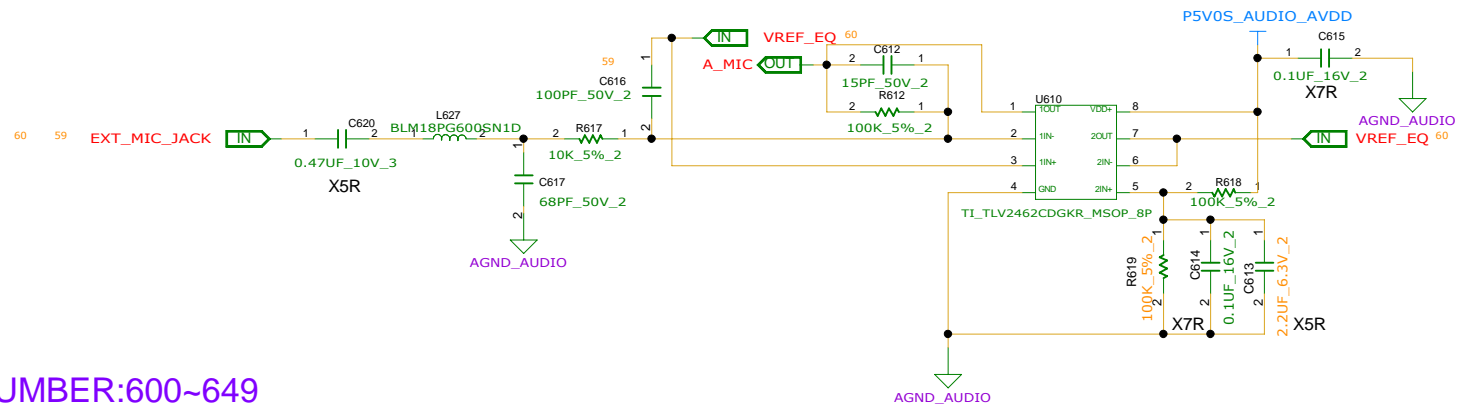
	5V	3.3V
R201	INSTALL	UNINSTALL
R202	INSTALL	UNINSTALL
R203	UNINSTALL	INSTALL
R204	INSTALL	UNINSTALL

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REFERENCE NUMBER:100~199

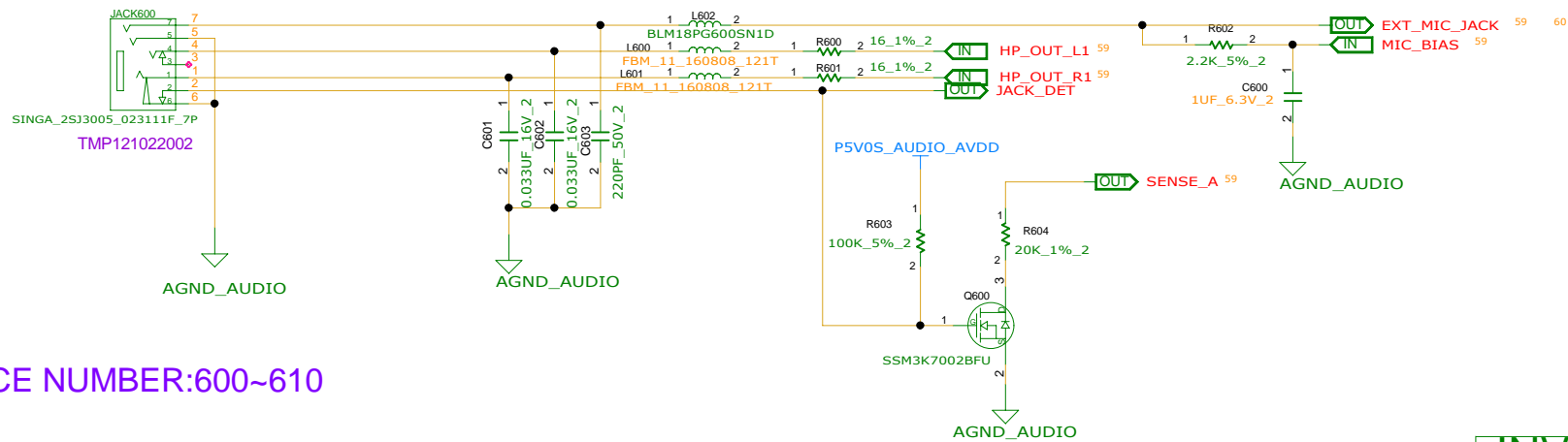
INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
STICK POINT & B2B CNTR			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET		58 of 77	



REFERENCE NUMBER:600~649

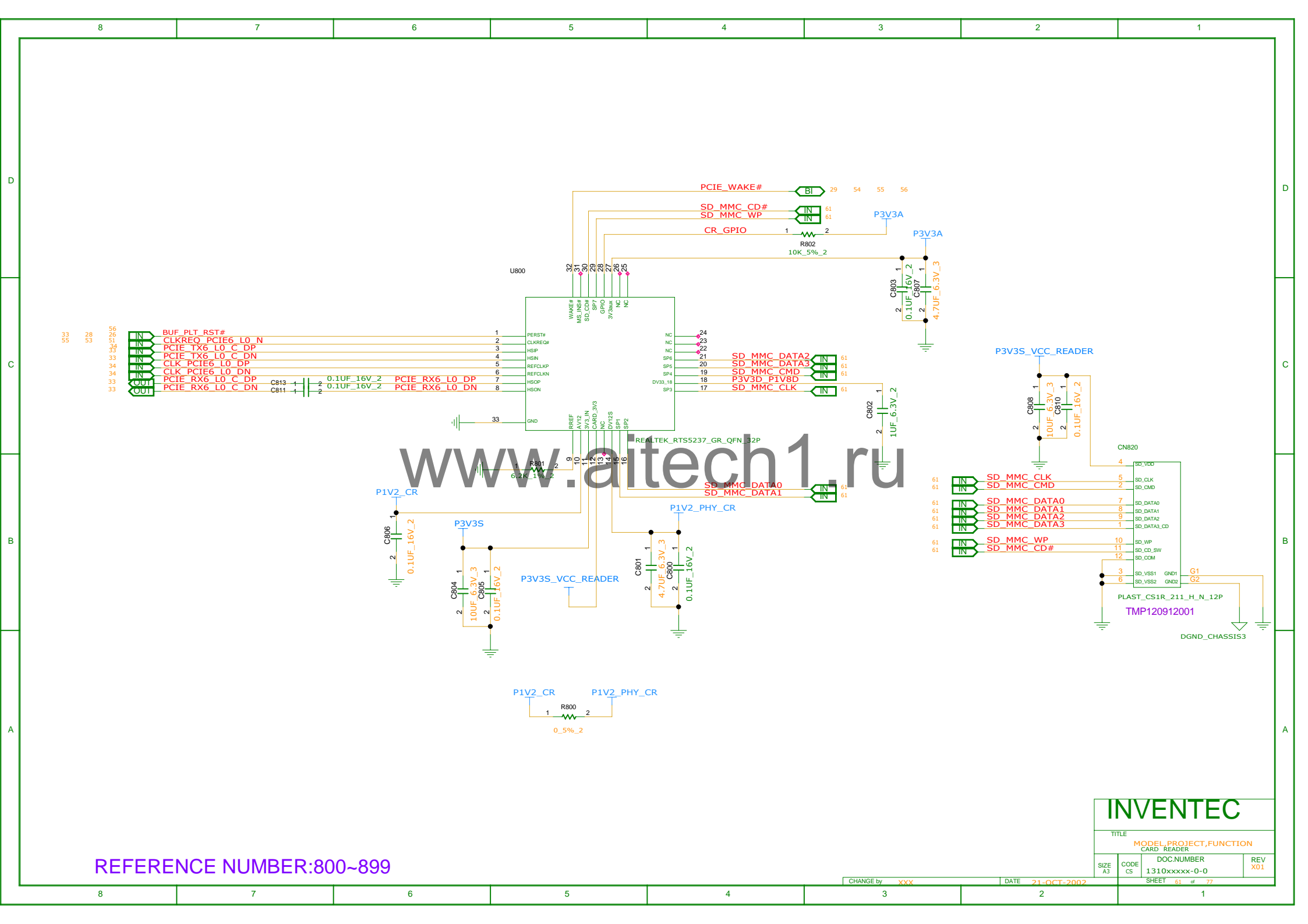
www.aitech1.ru



REFERENCE NUMBER:600~610

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
EXT. MIC AMP. & AUDIO JACK			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 60 of 77			



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REFERENCE NUMBER:800~899

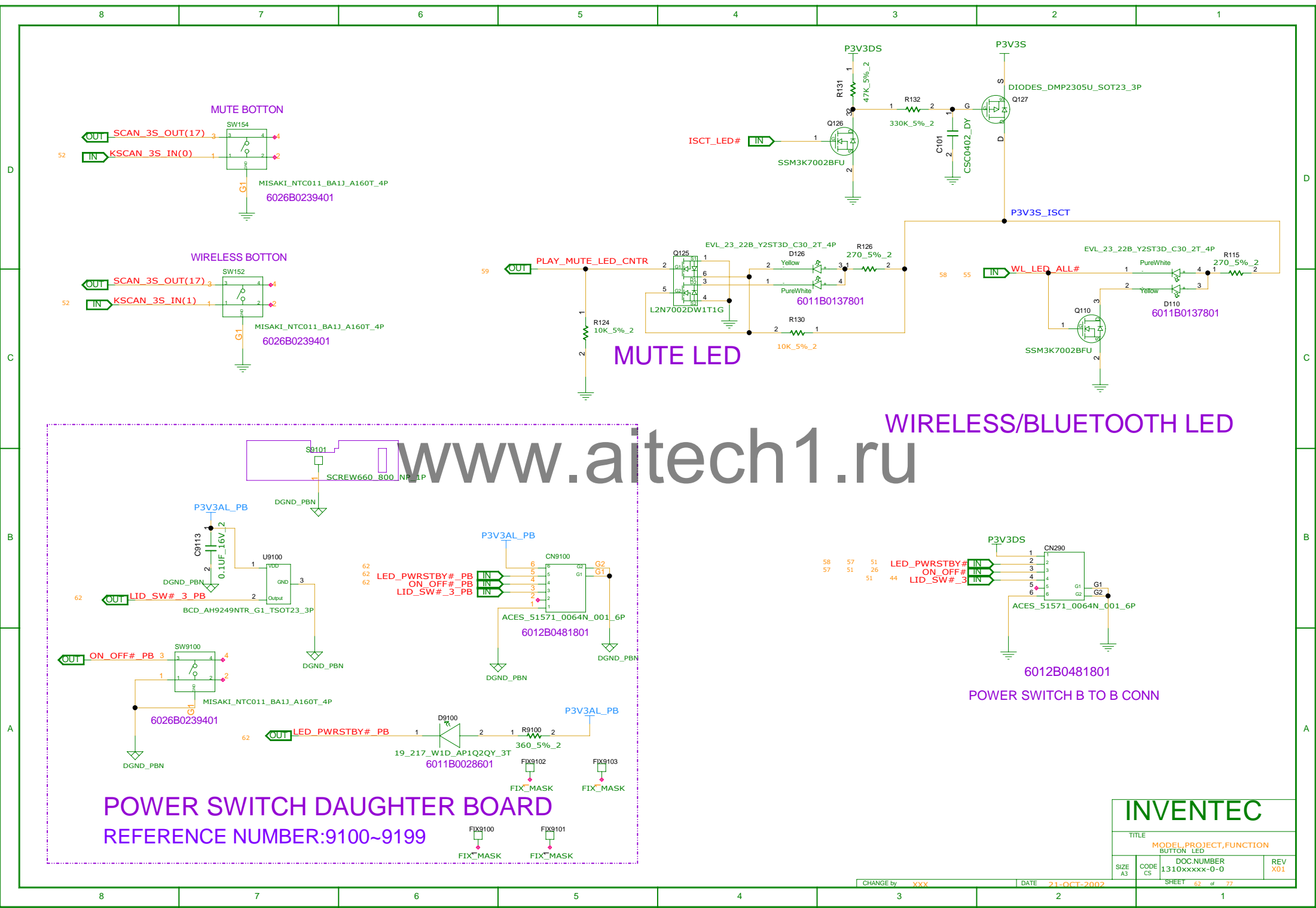
INVENTEC

TITLE
MODEL PROJECT,FUNCTION
CARD READER

SIZE	CODE	DOC.NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

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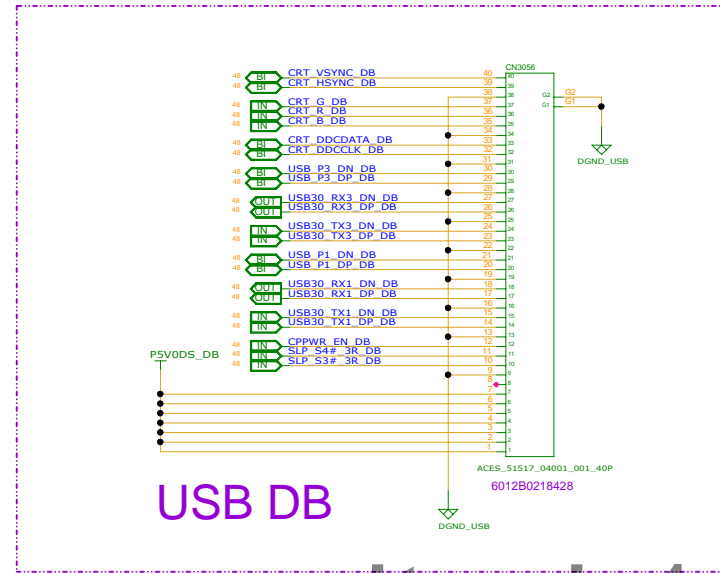


POWER SWITCH DAUGHTER BOARD
REFERENCE NUMBER:9100~9199

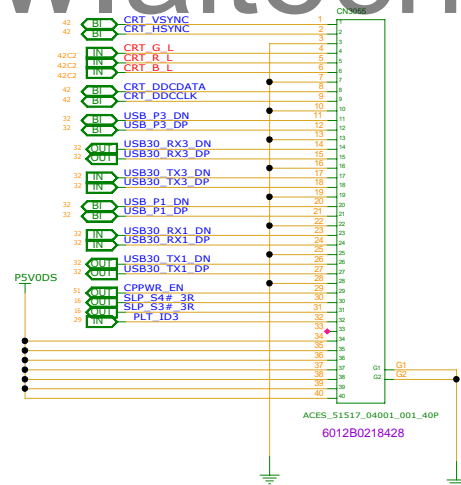
INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
BUTTON LED			
SIZE A3	CODE C3	DOC.NUMBER 1310xxxxx-0-0	REV X01
SHEET 62 of 77			

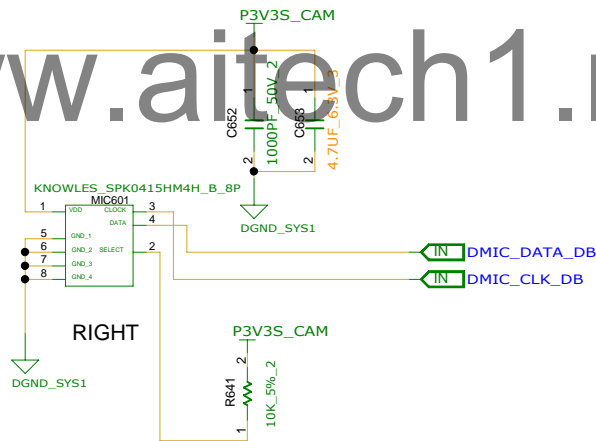
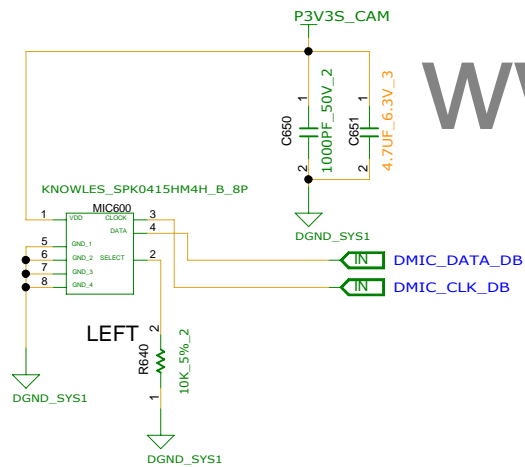
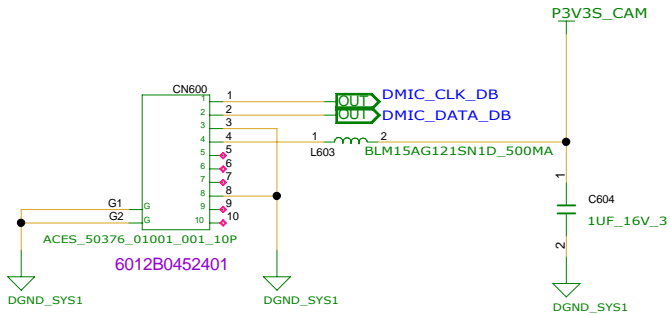
CHANGE by XXX DATE 21-OCT-2002



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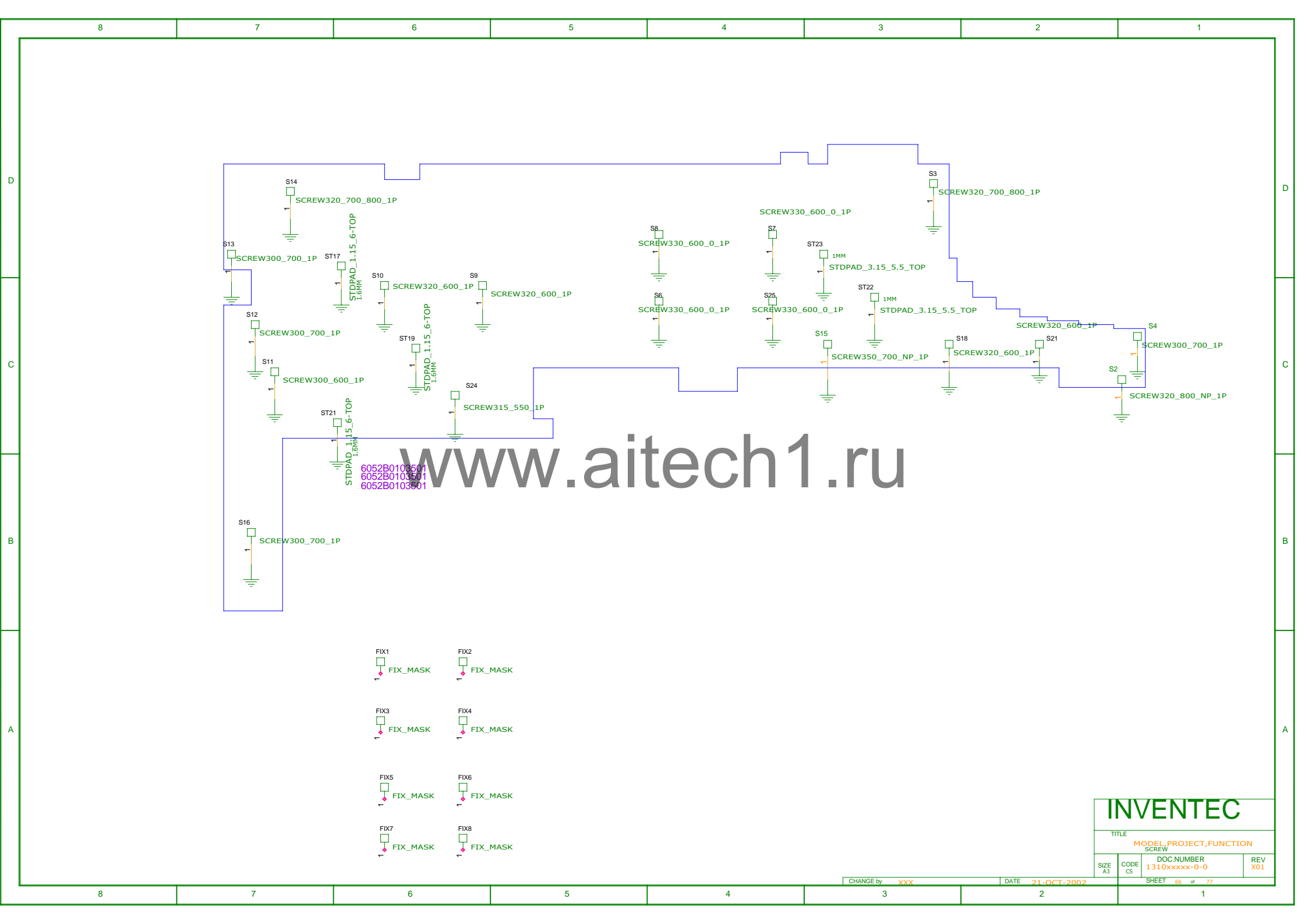


INVENTEC				
TITLE				
MODEL,PROJECT,FUNCTION				
RESERVE				
SIZE	CODE	DOC NUMBER	REV	
C	CS	1310XXXX-0-0	X01	
CHANGE BY		DATE	SHEET	
XXX		21-OCT-2002	64 of 67	



REFERENCE NUMBER:600-649
MIC DOUGHTER BOARD

INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 65 of 77			

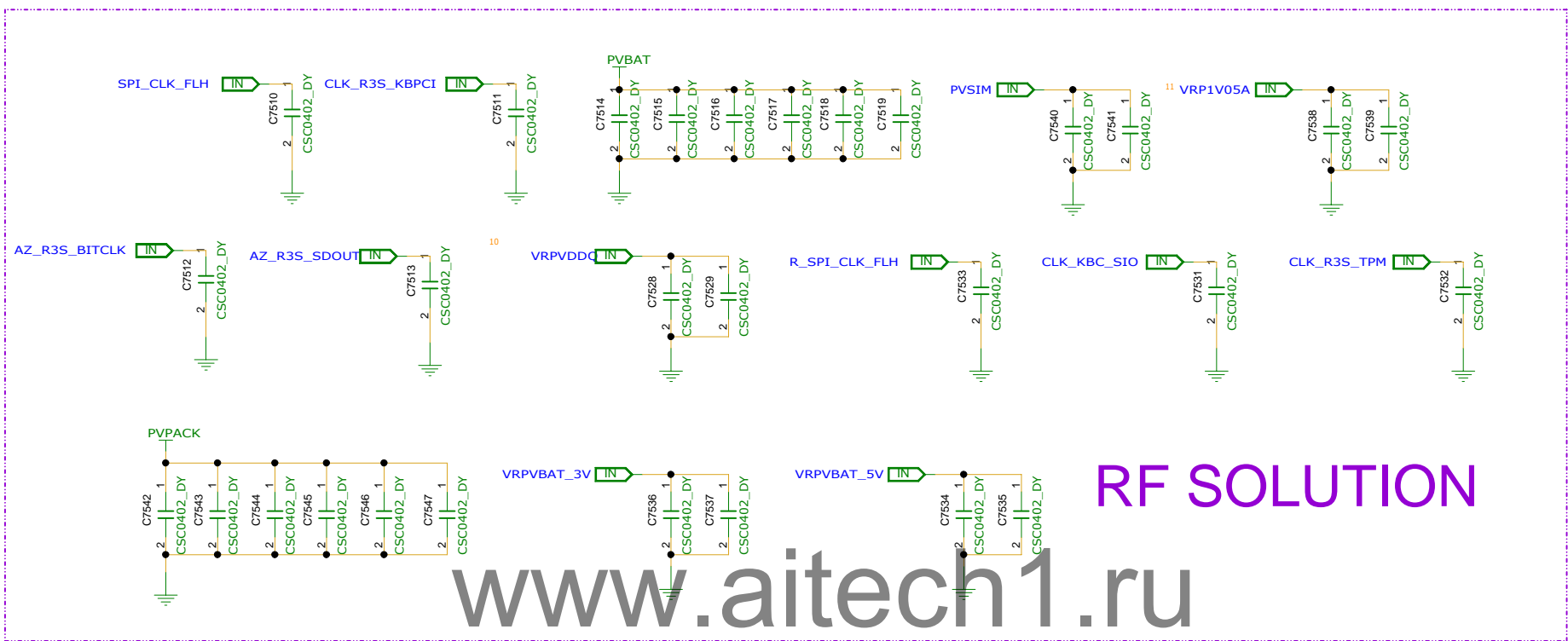


6052B0103501
6052B0103501
6052B0103501

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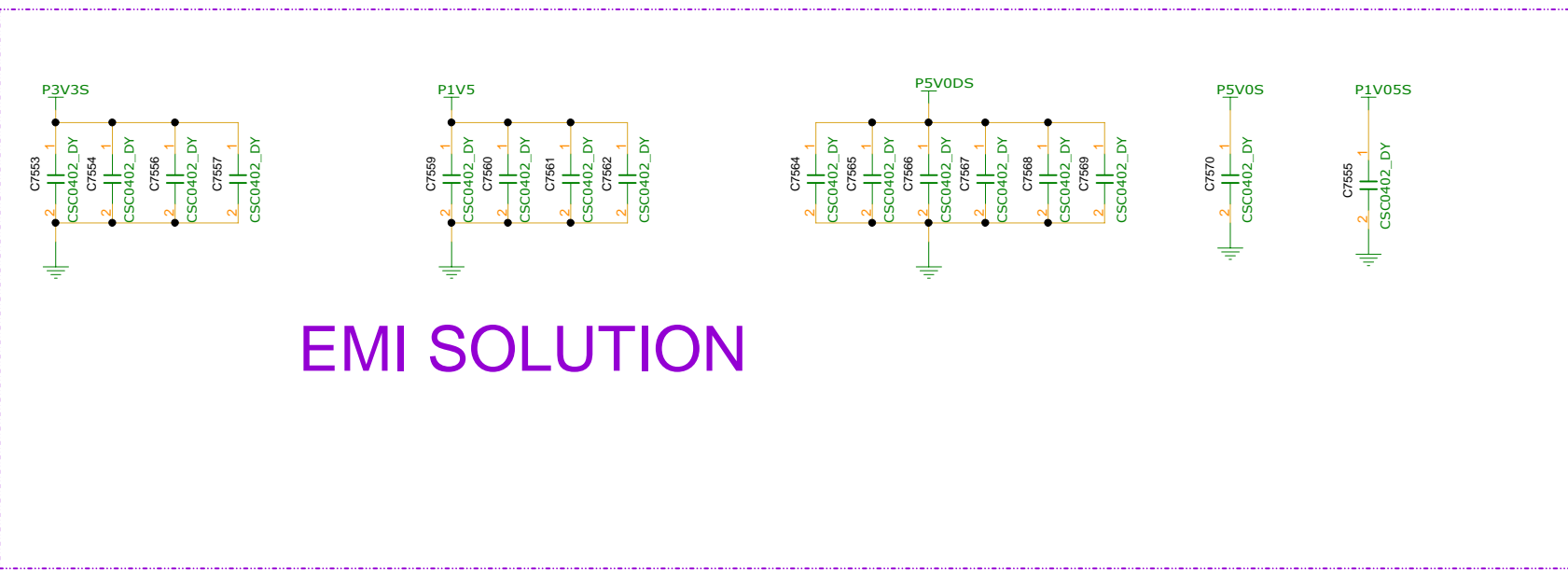
- FIX1
FIX_MASK
- FIX2
FIX_MASK
- FIX3
FIX_MASK
- FIX4
FIX_MASK
- FIX5
FIX_MASK
- FIX6
FIX_MASK
- FIX7
FIX_MASK
- FIX8
FIX_MASK

INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION SCREW			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 66 of 77			



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RF SOLUTION



EMI SOLUTION

INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
EMI & RF SOLUTION			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 67 of 77			

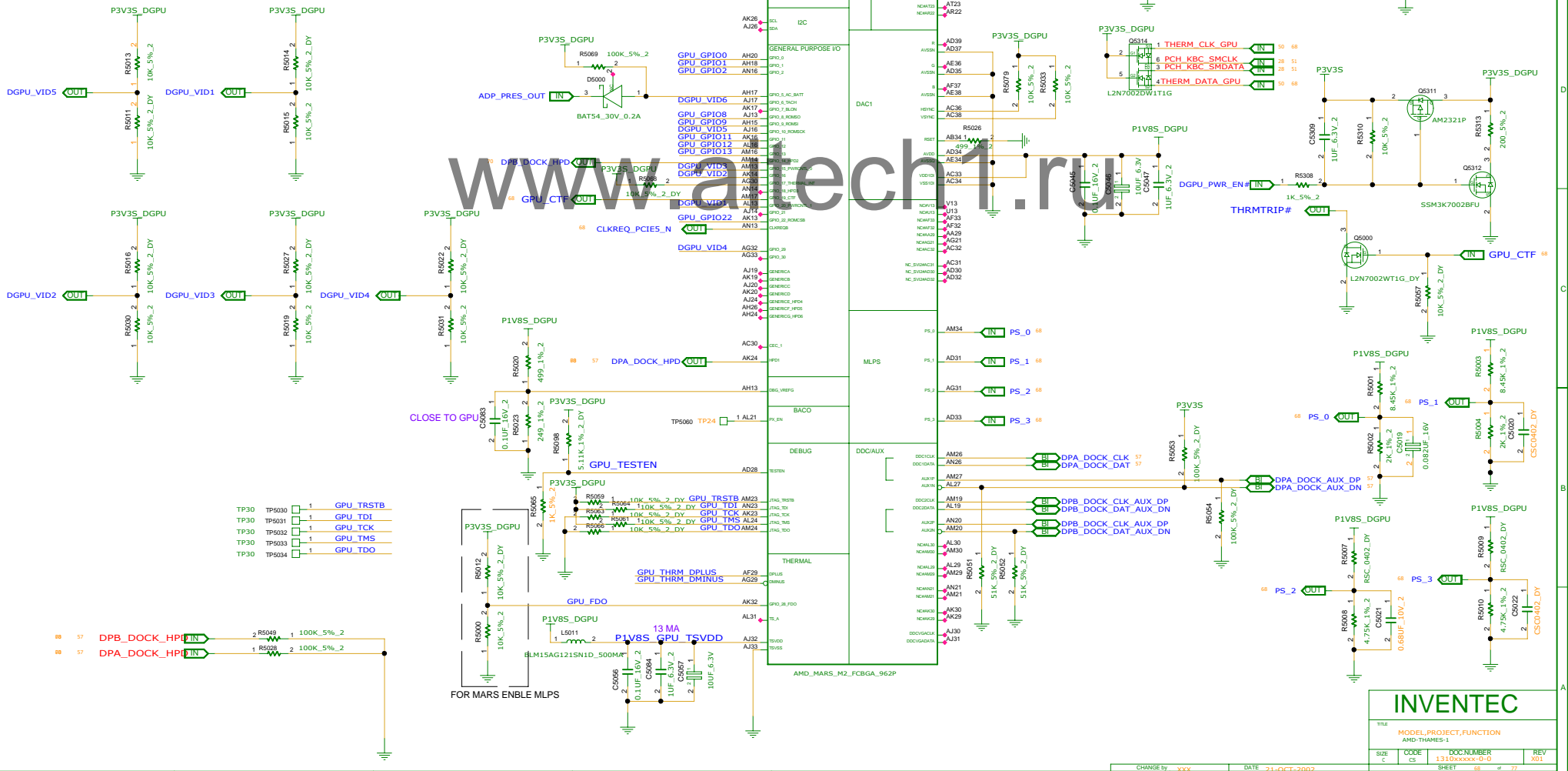
MLPS TABLE

Size of the Primary Memory Apertures	ROM_CONFIG[2:0]
128 MB	000
256 MB	001
64 MB	010
Reserved	011

R _{pu} (Ω)	R _{pd} (Ω)	Bits [3:1]
NC	4750	000
8450	2000	001
4530	2000	010
6980	4990	011
4530	4990	100
3240	5620	101
3400	10000	110
4750	NC	111

Note: 0402 1% resistors are required.

Capacitor Value (nF)	Bits [5:4]
680	00
82	01
10	10
NC	11



INVENTEC

TITLE	MODEL,PROJECT,FUNCTION
SIZE	CODE
DATE	DOC NUMBER
SHEET	REV

LPT-LP GPIO 34	MARS MLPS Bit: PS_3 [3:1]			R _{pu} (Ω)	R _{pd} (Ω)	Vendor & PN	Die Ver.	
0	0	0	0	NC	4750	Samsung - K4G20325FD-FC04	D	GDDR5 - 64Mx32/128Mx16, 1.5V/1.35V, 5.0Gbps/4Gbps
	0	0	1	8450	2000	Hynix - H5GQ2H24AFR-T2C	A (Gemma)	GDDR5 - 64Mx32/128Mx16, 1.5V/1.35V, 5.0Gbps/4Gbps
1	1	1	0	3400	10000	*Samsung - K4G41325FC-HC04	C	*GDDR5 - 128Mx32/256Mx16, 1.5V/1.35V, 5.0Gbps/4Gbps
	1	1	1	4759	NC	*Hynix - H5GC4H24MFR-T0C	Huma	*GDDR5 - 128Mx32/256Mx16, 1.5V/1.35V, 5.0Gbps/4Gbps
VBIOS selection :	Vram configuration			Vendor ID		Vram information		
0 : VBIOS 1, 64Mx32 for 1GB sku	00: 64Mx32 (2Gb)			0: Samsung		* 2GB sku, TBD		
1 : VBIOS 2, 128Mx32 for 2GB sku	11: 128Mx32 (4Gb)			1: Hynix				
				Resistor Divider				
				Lookup Table				

MLPS Implementation

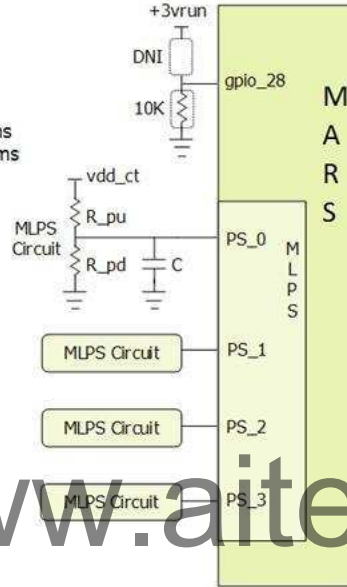
- Connect GPIO_28 to 10K pulldown to enable MLPS
- If any of PS_0/1/2/3 is not used, leave "no connect"
- R_{pu}, R_{pd} and C must be properly populated per tables below
- Place MLPS circuit components as close to the ASIC as possible
- Total DC resistance of trace between PS pin and C should be less than 2 ohms
- Total DC resistance of trace between C and ground should be less than 2 ohms
- Trace capacitance should be less than 100pF. Resistors should be of +/-1% tolerance

Capacitor Lookup Table

C (nF)	Bits(5,4)
680	00
82	01
10	10
NC	11

Resistor Divider Lookup Table

R _{pu} (Ohm)	R _{pd} (Ohm)	Bits(3,2,1)
NC	4750	000
8450	2000	001
4530	2000	010
6980	4990	011
4530	4990	100
3240	5620	101
3400	10000	110
4750	NC	111



MLPS Bit	Strap Name	Description
PS_0[1]	ROM_CONFIG[0]	See Primary Memory Aperture Sizes.
PS_0[2]	ROM_CONFIG[1]	
PS_0[3]	ROM_CONFIG[2]	
PS_0[4]	N/A	Reserved for internal use only. Must be 1 at reset.
PS_0[5]	AUD_PORT_CONN_PINSTRAP[0]	The LSB (least significant bit) of the strap that indicates the number of audio-capable display outputs.
PS_1[1]	STRAP_BIF_GEN3_EN_A	PCIe GEN3 capability. 1 = PCIe GEN3 is supported.
PS_1[2]	STRAP_BIF_CLK_PM_EN	Determines whether or not the PCIe reference clock power management capability is reported in the PCI configuration space (otherwise known as CLKREQB). 0 = The CLKREQB power management capability is disabled
PS_1[3]	N/A	Reserved for internal use only. Must be 0 at reset.
PS_1[4]	STRAP_TX_CFG_DRV_FULL_SWING	Control the transmitter full-/half-swing mode 1 = The transmitter full-swing is enabled
PS_1[5]	STRAP_TX_DEEMPH_EN	PCI EXPRESS® transmitter, de-emphasis enable. 1 = Tx deemphasis enabled.
PS_2[1]	N/A	Reserved.
PS_2[2]	N/A	Reserved.
PS_2[3]	STRAP_BIOS_ROM_EN	To enable the external BIOS ROM device. 0 = Disable the external BIOS ROM device.
PS_2[4]	STRAP_BIF_VGA_DIS	VGA disable determines whether or not the card will be recognized as the system's VGA controller (through the SUBCLASS field in the PCI configuration space). 0 = VGA controller capacity enabled.
PS_2[5]	N/A	Reserved.
PS_2[1]	N/A	Reserved.
PS_2[2]	N/A	Reserved.
PS_2[3]	STRAP_BIOS_ROM_EN	To enable the external BIOS ROM device. 0 = Disable the external BIOS ROM device.
PS_2[4]	STRAP_BIF_VGA_DIS	VGA disable determines whether or not the card will be recognized as the system's VGA controller (through the SUBCLASS field in the PCI configuration space). 0 = VGA controller capacity enabled.
PS_2[5]	N/A	Reserved.
PS_3[1]	BOARD_CONFIG[0]	See Board configuration related strapping, such as for memory ID.
PS_3[2]	BOARD_CONFIG[1]	
PS_3[3]	BOARD_CONFIG[2]	
PS_3[4]	AUD_PORT_CONN_PINSTRAP[1]	Determines the maximum number of digital display audio endpoints that will be presented to the OS and user. 111 = No usable endpoints. 110 = One usable endpoint. 101 = Two usable endpoints. 100 = Three usable endpoints. 011 = Four usable endpoints. 010 = Five usable endpoints. 001 = Six usable endpoints. 000 = All endpoints are usable.
PS_3[5]	AUD_PORT_CONN_PINSTRAP[2]	

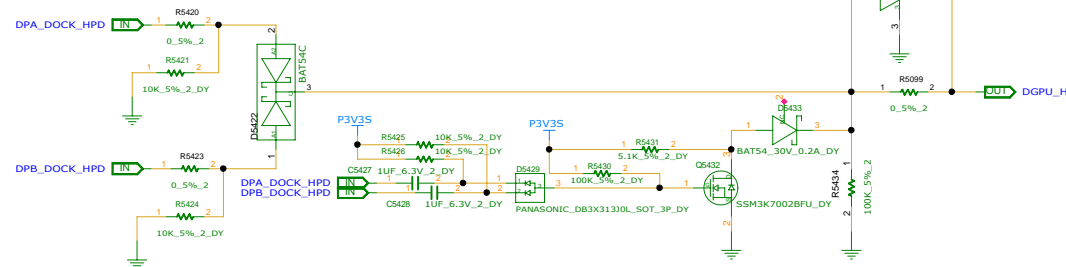
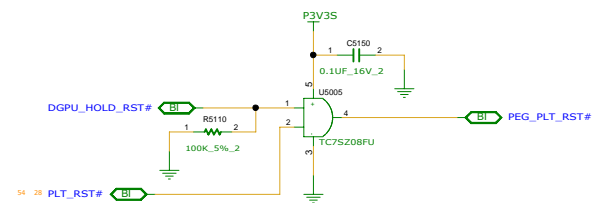
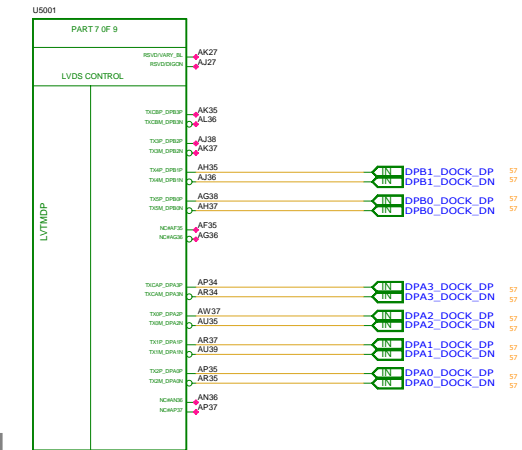
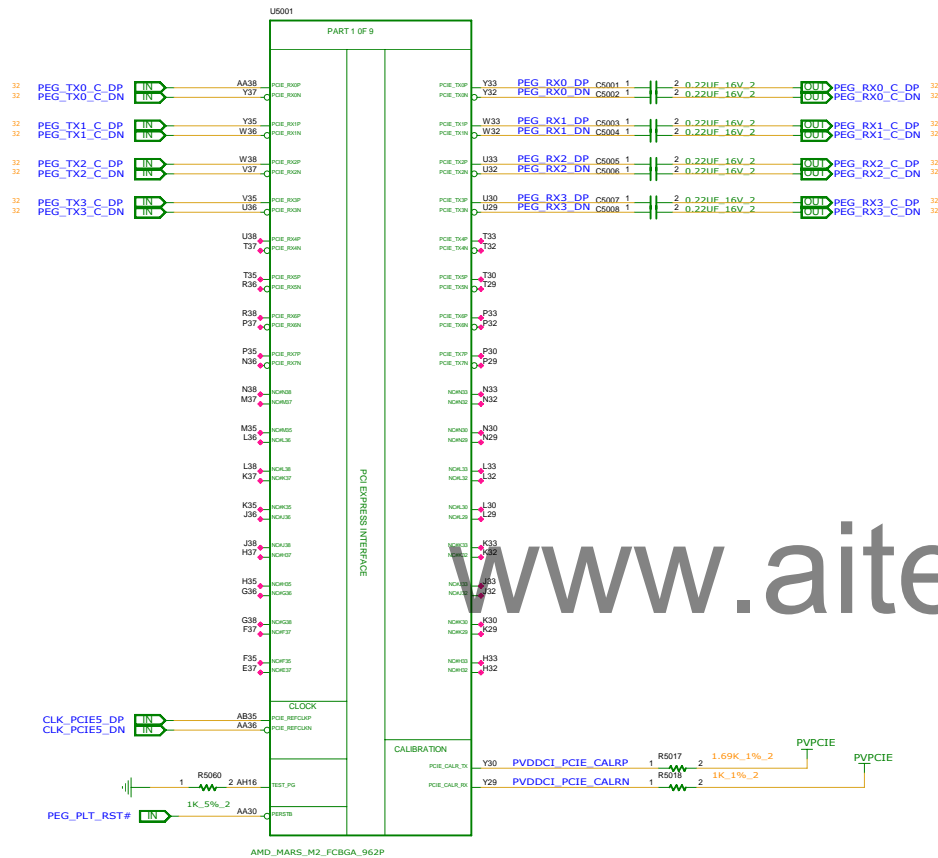
Note : AUD[1] (on HYSNC) and AUD[0] (on VSYNC) still need to be properly pin strapped even in a MLPS-based design.

Pin/Bit	Name	Description	Default	Legacy
PS_0[3:1]	romidcfg[2:0]	Memory aperture size or ROM type select: If bios_rom_en = 0, romidcfg[2:0] define memory aperture size If bios_rom_en = 1, romidcfg[2:0] define ROM type	xxx	gpio_13 gpio_12 gpio_11
PS_0[4]	n/a	Reserved	1	genlk_vsync
PS_1[1]	bif_gen3_en_a	PCIe Gen3 capability: 1=Gen3 supported, 0=Gen3 not supported	x	gpio_2
PS_1[2]	bif_clk_pm_en	PCIe Clk PM capability: 1 = CLKREQB supported	x	gpio_8
PS_1[3]	n/a	Reserved		genlk_clk
PS_1[4]	tx_pwrs_enb	PCIe Tx power savings: 0=50% swing, 1=full swing	x	gpio_0
PS_1[5]	tx_deemph_en	PCIe Tx de-emphasis: 1=Tx de-emphasis enabled	x	gpio_1
PS_2[1]	n/a	Reserved		n/a
PS_2[2]	n/a	Reserved		n/a
PS_2[3]	bios_rom_en	Enable external BIOS ROM: 1=External ROM connected	x	gpio_22
PS_2[4]	vga_dis	VGA disable: 1=Disable this GPU as the system's VGA controller	0	gpio_9
PS_2[5]	n/a	Reserved		n/a
PS_3[1]	MEM Vendor ID	MEM Vendor ID	0	n/a
PS_3[2]	MEM Vendor ID	MEM Vendor ID	0	n/a
PS_3[3]	MEM Vendor ID	MEM Vendor ID	0	n/a
PS_3[5]	aud_port_cp[2]	3-bit field indicating number of audio-capable display outputs	xxx	n/a
PS_3[4]	aud_port_cp[1]			
PS_0[5]	aud_port_cp[0]			

INVENTEC

MODEL, PROJECT, FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
C	CS	1310XXXX-0-0	201
SHEET		of	17

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INVENTEC			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
C	CS	13101000000-0-0	201
SHEET		36	of 37

CHANGE by: xxx DATE: 21-OCT-2002

VM_DQB0_<31..0>

VM_DQB1_<31..0>

P1V35S_DGPU

P1V35S_DGPU

AMD_MARS_M2_FCBGA_962P

INVENTEC

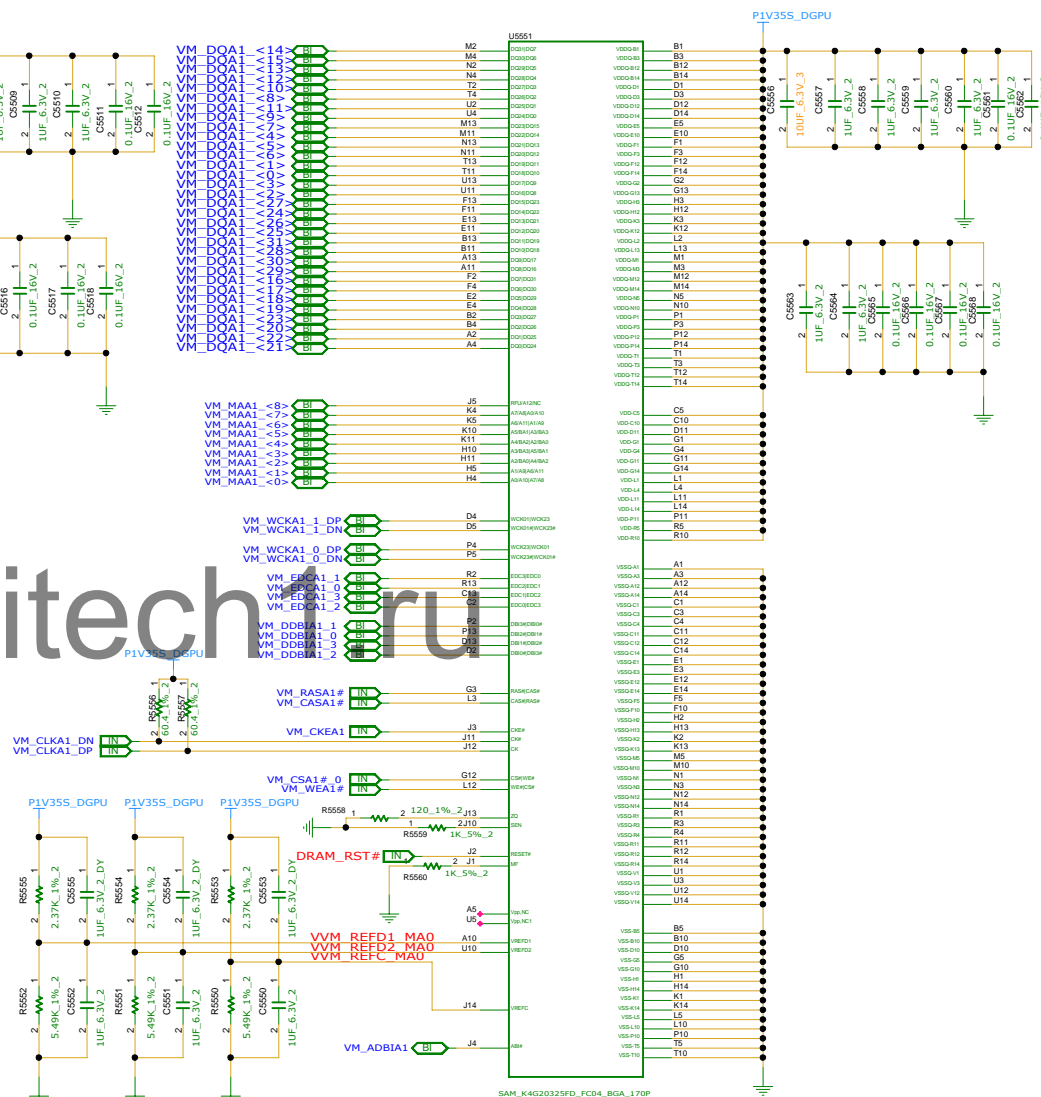
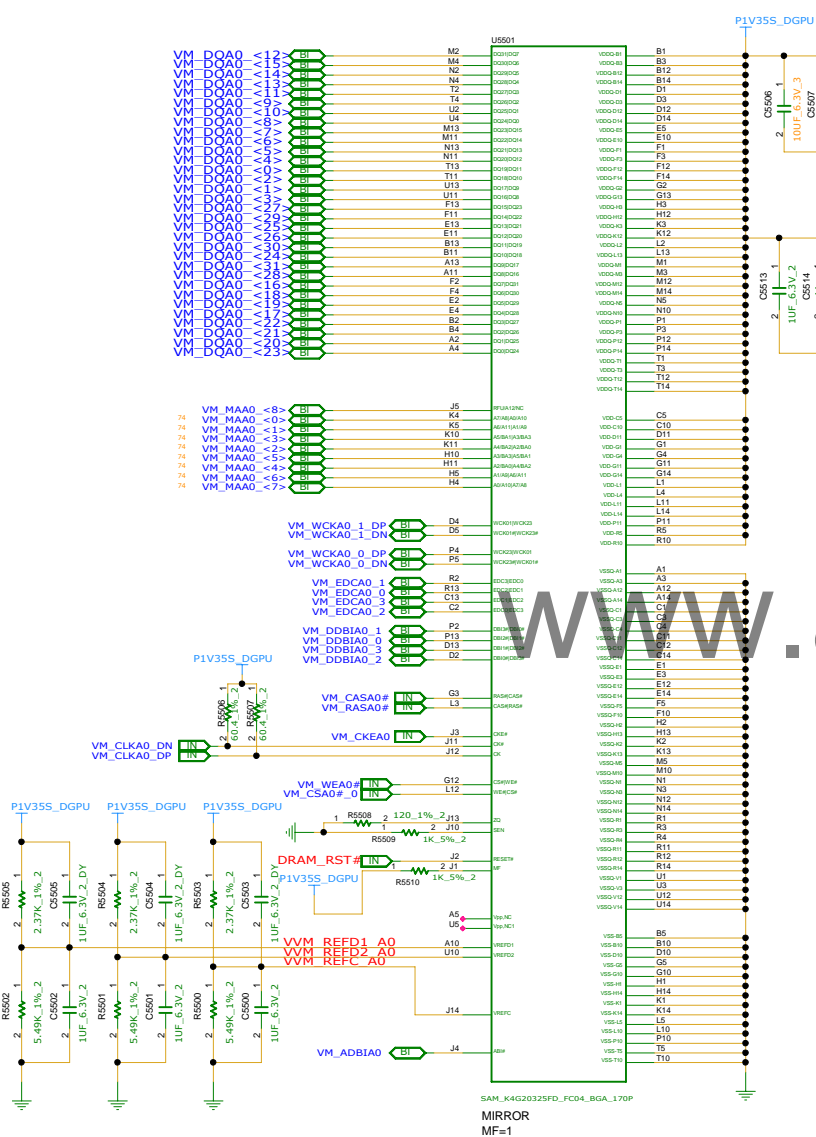
TITLE
MODEL PROJECT FUNCTION
AMD THAMES-3

DOC NUMBER
1310xxxxx-0-0

REV
X01

CHANGE by XXX DATE 21-OCT-2002

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MIRROR
MF=1

CHANGE by	XXX	DATE	21-OCT-2002
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TITLE			
MODEL,PROJECT,FUNCTION			
VRAM-2			
SIZE C	CODE CS	DOC NUMBER 1310xxxxxx-0-0	REV X01
SHEET 77 of 77			